

SPIO
SYSTEMS

Miniaturised Optical Engines on Wafer Level via SPIO Technology

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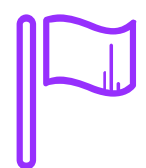
About SPIO Systems

SPIO Systems is a deep-tech company.

SPIO has developed/invented a radically new
manufacturing technology of optical engines/devices

The SPIO technology enables miniaturization of existing
optical engines by at least an order of magnitude

The SPIO technology enables cost-effective, mass
production of optical engines in millions of units, which
were not possible with previous manufacturing
technologies



Founded in
September 2020

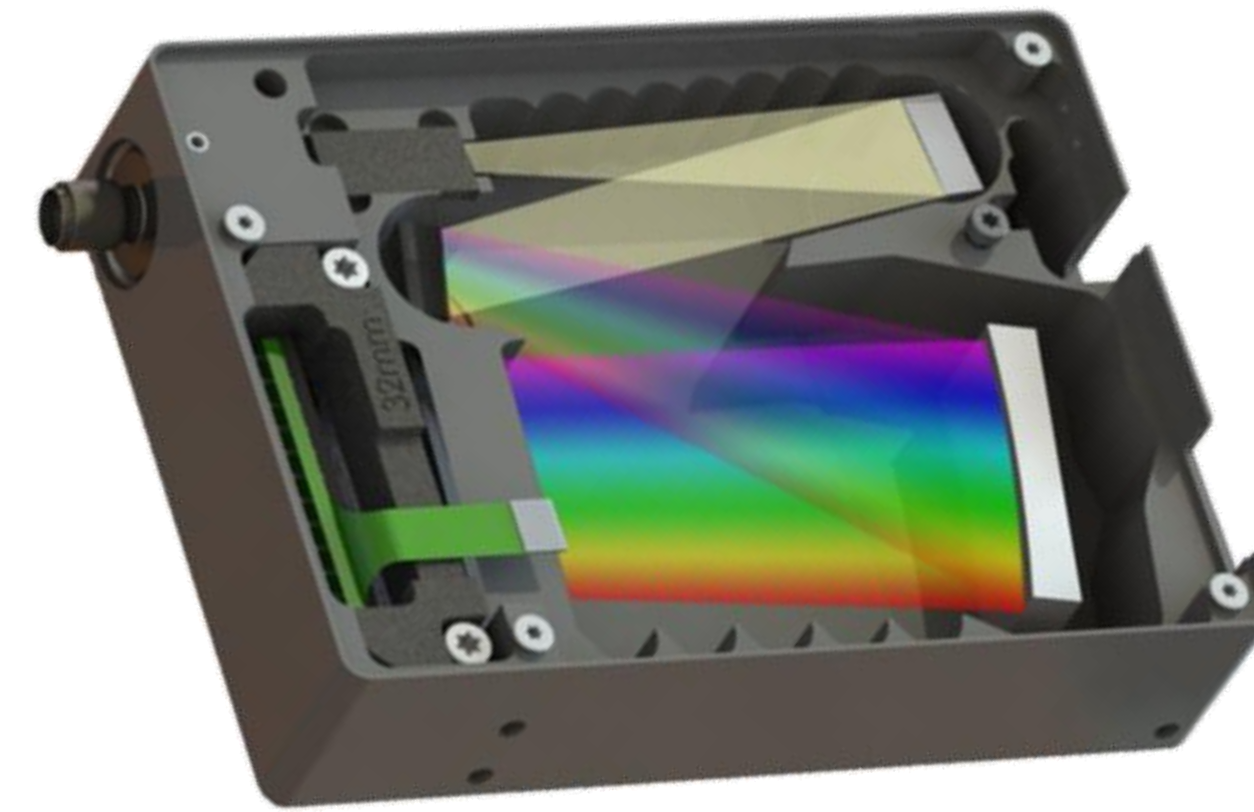


Based in Farum,
Denmark



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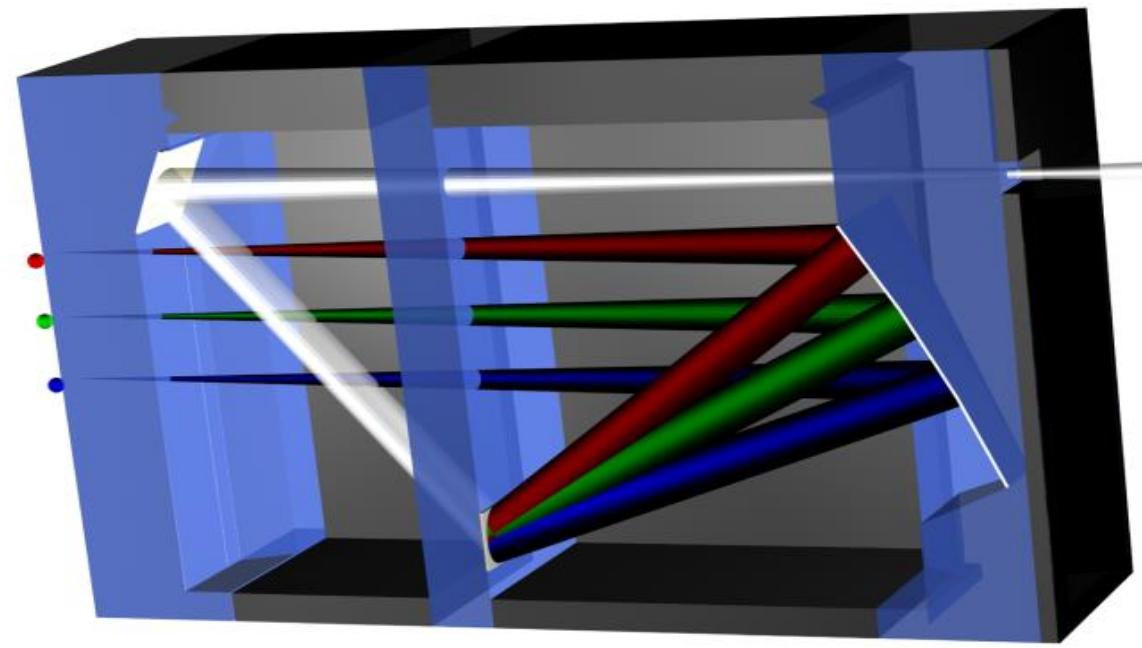
SPIO stands for
Stacked
Planar
Integrated
Optics



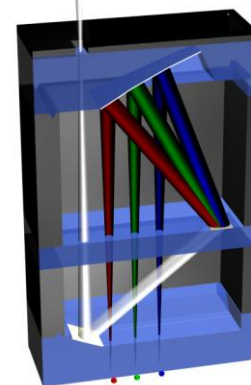
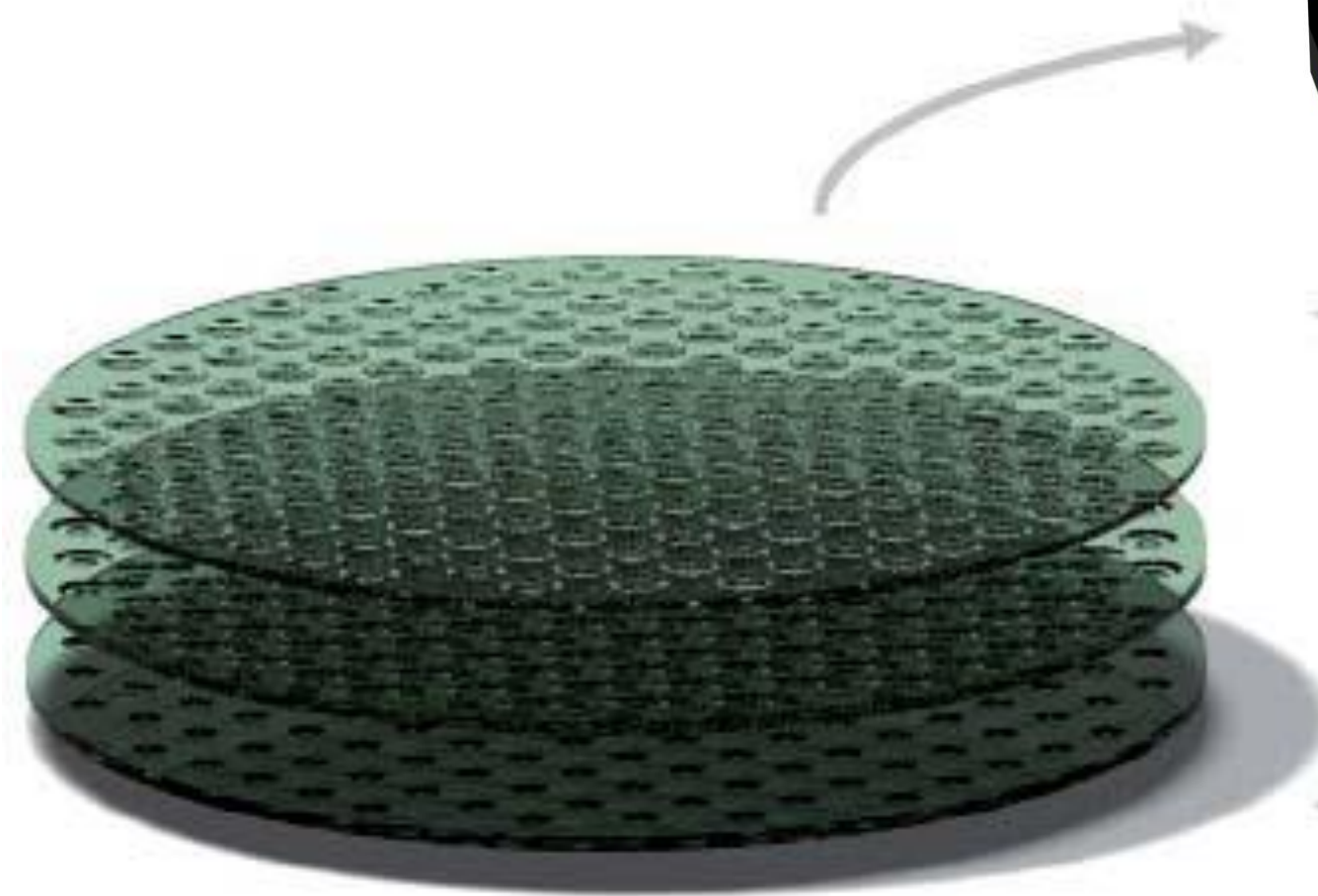
Current optical engine manufacturing method

- ✗ **Discrete** optical elements into box
- ✗ Extensive **assembly** process: Account for +70% **cost**
- ✗ Lacking **scalability** to deliver mass volume
- ✗ Lacking **robustness** to environmental impact

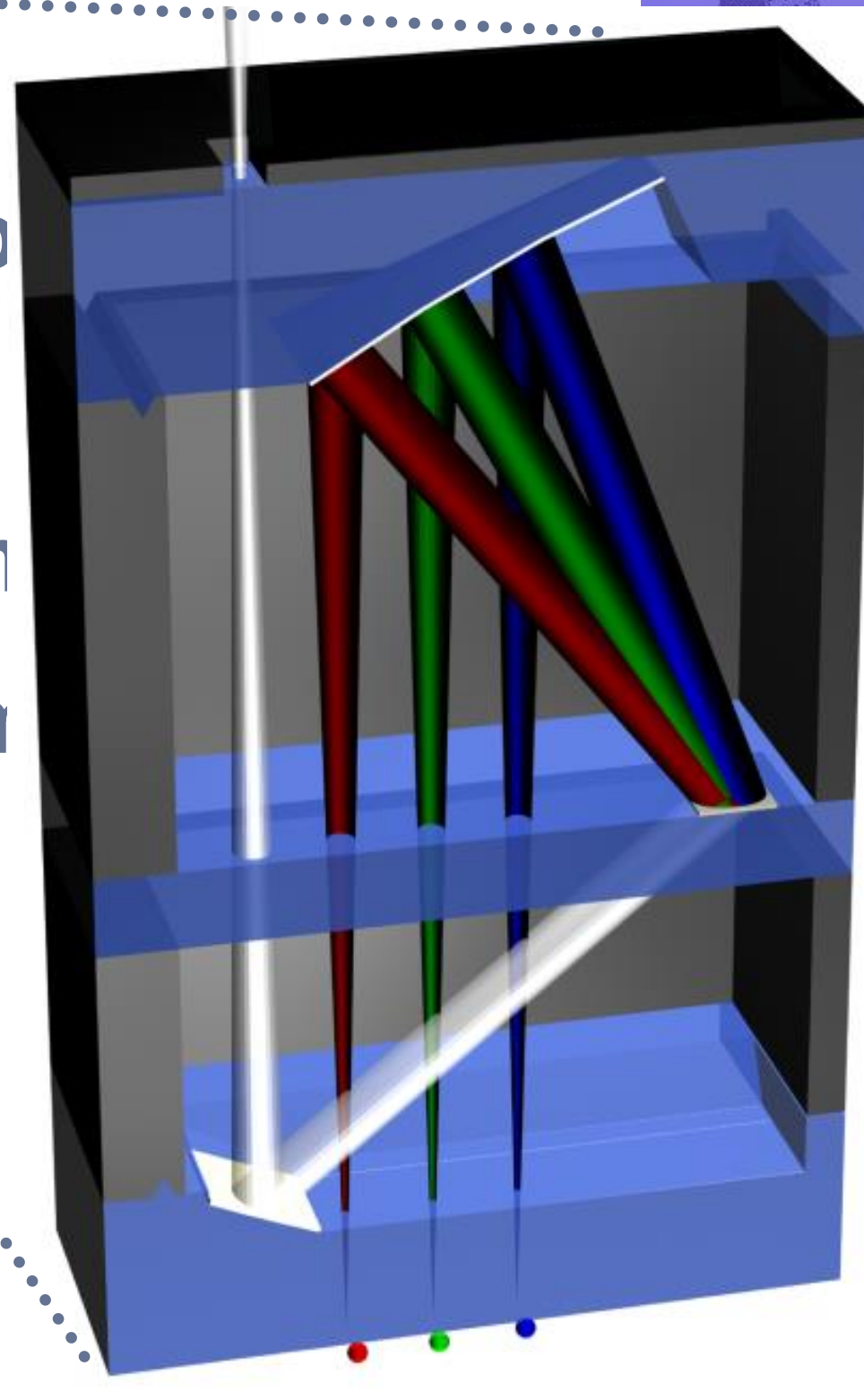
Optics design & manufacturing – a market ready for *disruption*



Introducing **SPIO Technology** in optical engine manufacturing



one go: Low
Reduce 80%
s equipment & n
environmental in



Optics design & manufacturing – a market ready for *disruption*

SPIO vs. Conventional optics manufacturing



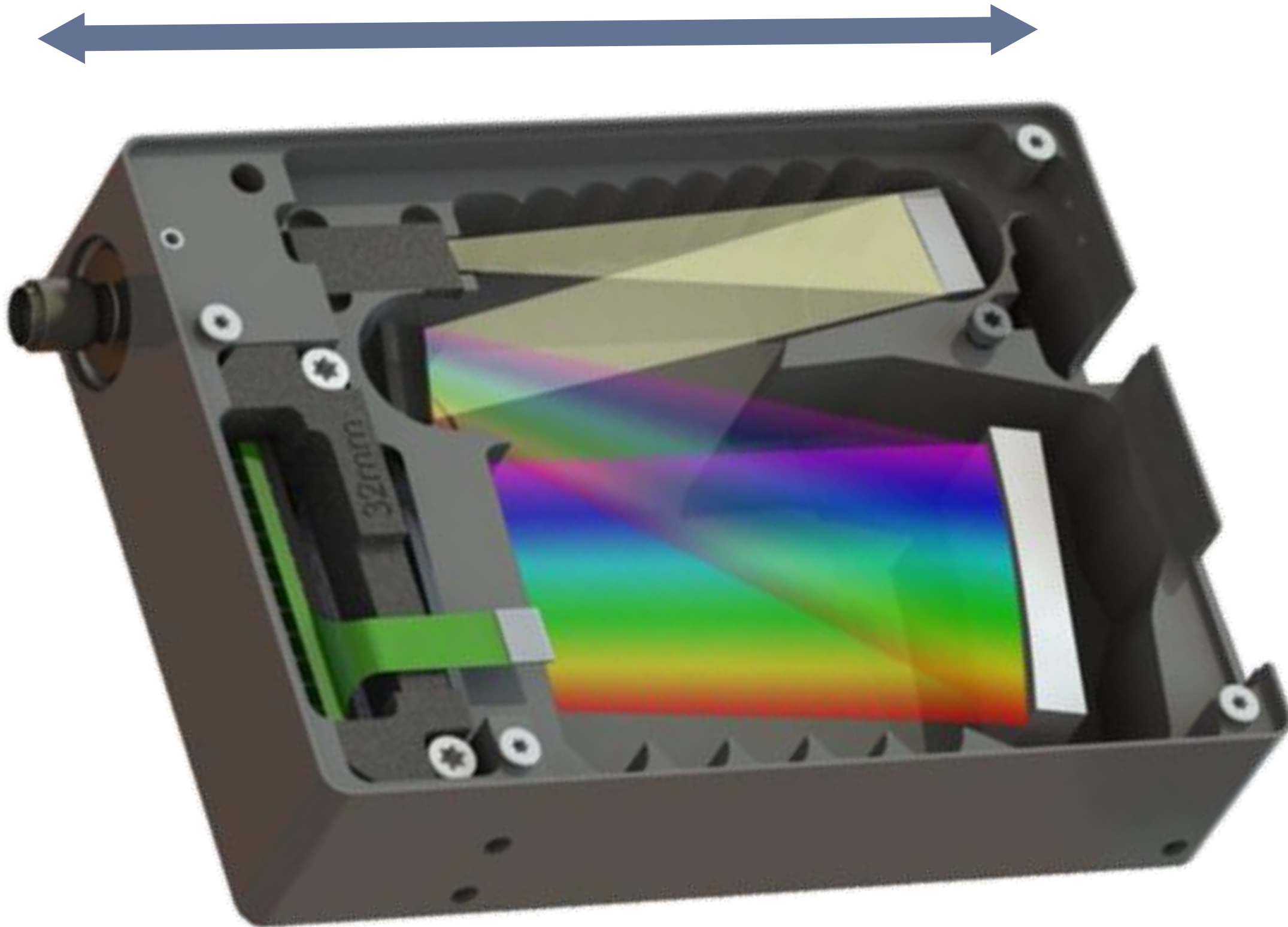
Same optical device but different **space occupation** and **production cost** and **scaling rate**

CONVENTIONAL DEVICE: €€€€€

Discrete optical elements

Active alignment and manual assembly

50mm

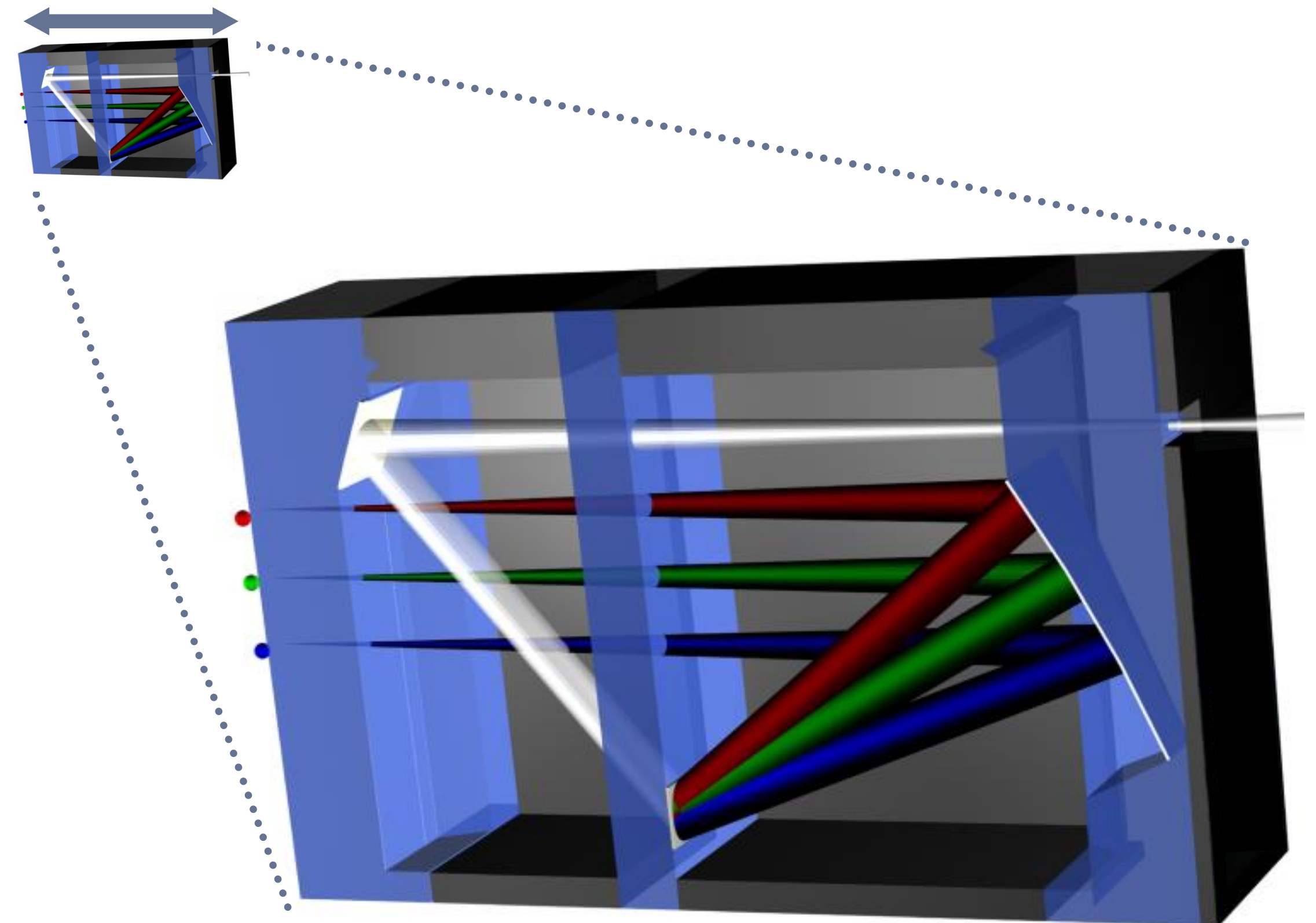


SPIO DEVICE: €

Integrated optical components in single planar layers (wafer)

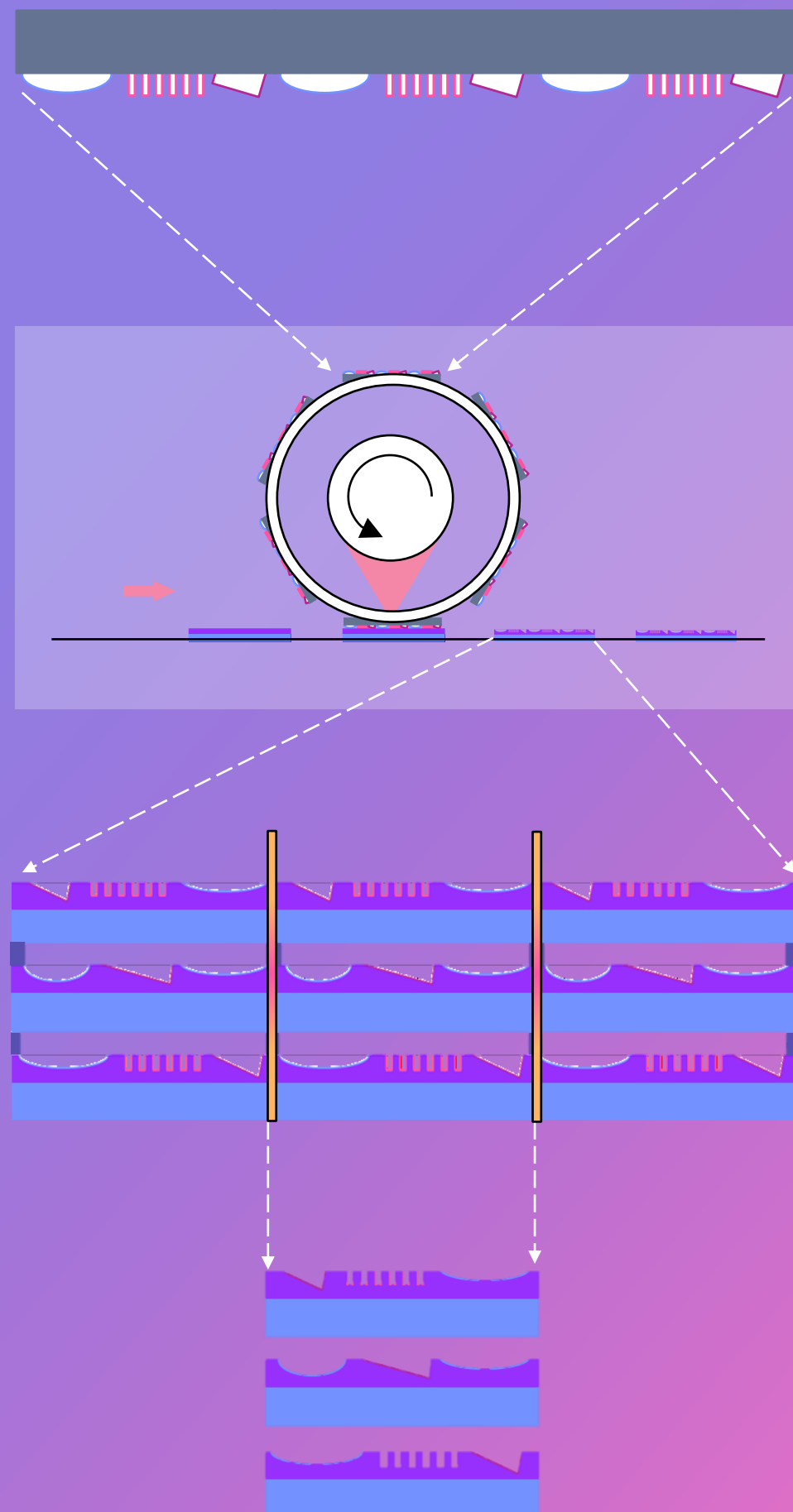
Passive alignment and automated assembly

2mm



Technology

LOW COST, MASS PRODUCTION OF
COMPLEX OPTICAL SYSTEMS



1. Master design and production

2. Roll-to-plate transfer to polymer on glass wafers

3. Stacking and characterization

4. Dicing into individual components



Master that enables compact and complex optics



Cheap and fast production process



Stacking of wafers: Assembly thousands of optical devices in parallel



A very compact 3D optical SPIO device with a high dense of light processing. A manufacturing technology that makes SPIO Systems unique

What is SPIO?

SPIO IS...

A technology platform that enables guiding and processing of light in advanced, complex optical structures.

- A toolbox of optical elements made to a single component
- Closed packed design: No mechanics on individual elements

- “Cubic” design: Optical light paths in 3 dimensions – in 2D planar layers but between layers as well

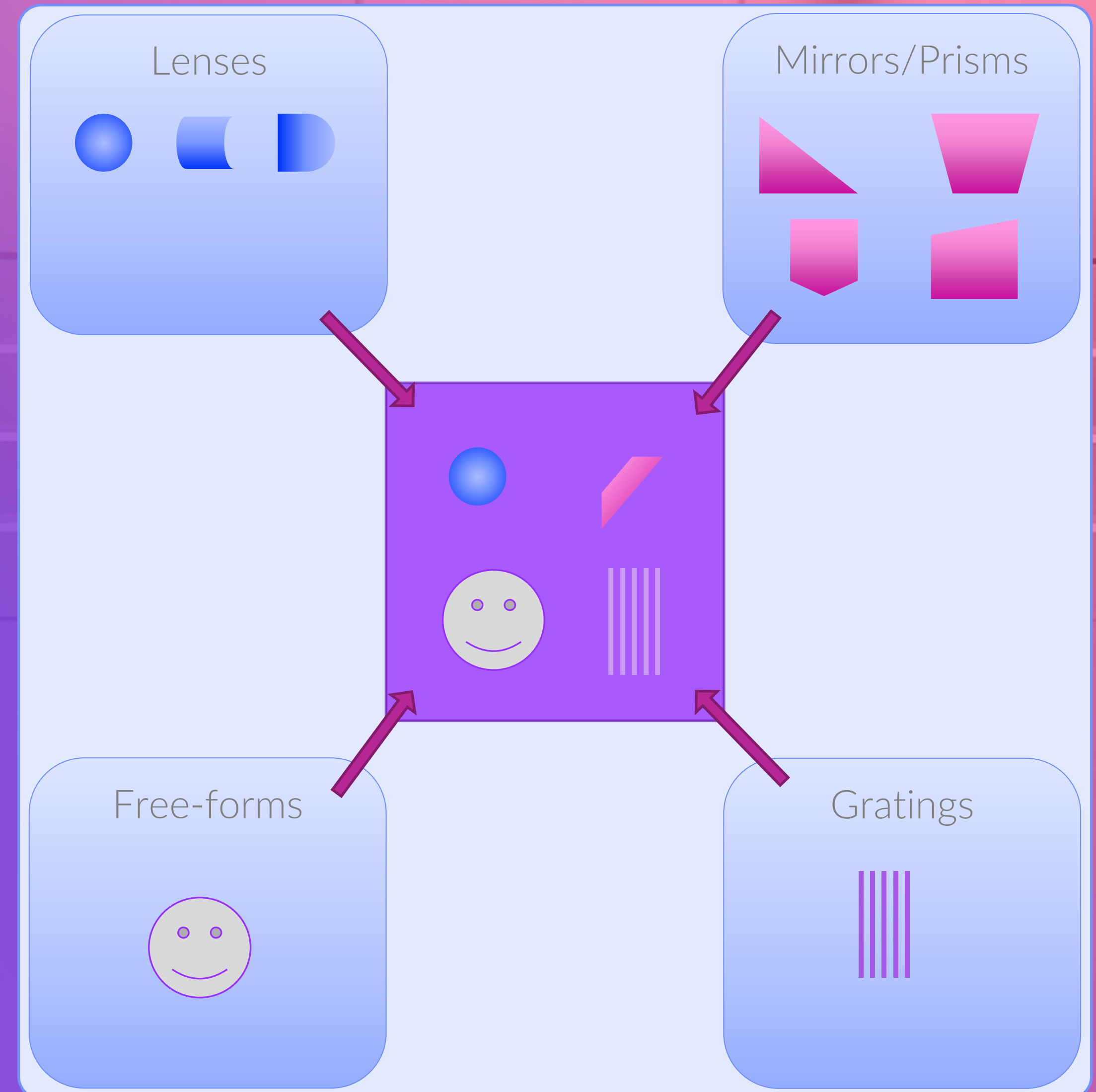
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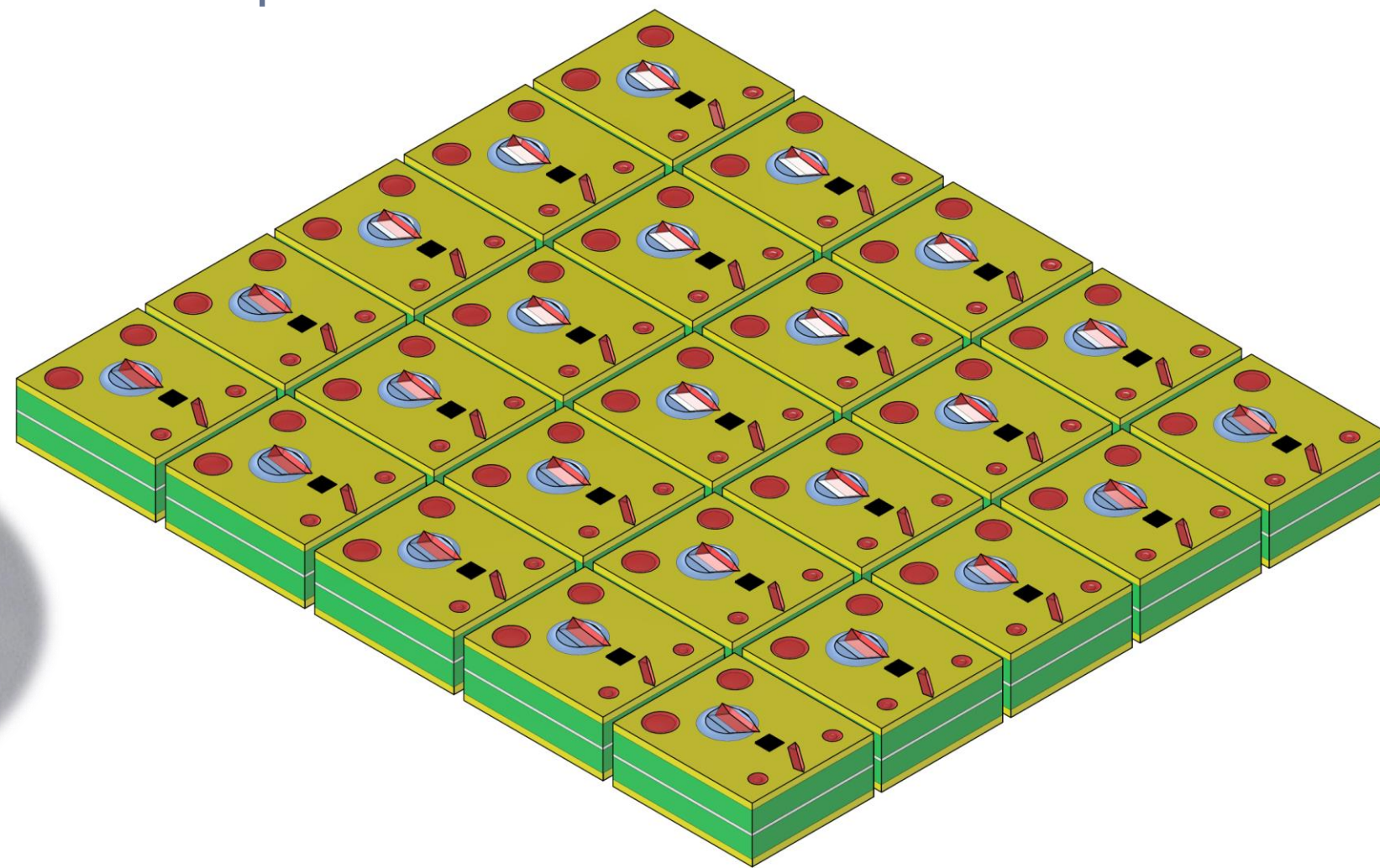
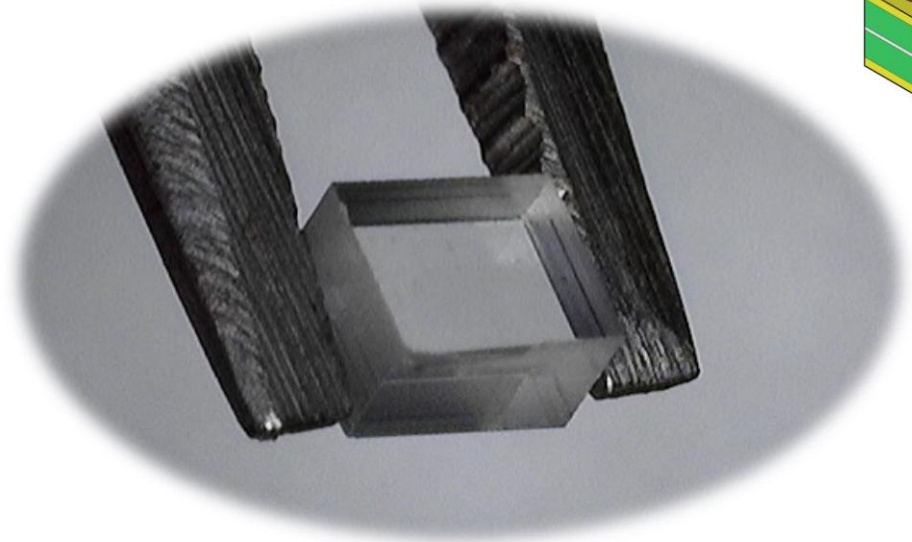


What is SPIO?

SPIO IS...

A **manufacturing method** that uses advanced Nanoimprint Lithography (NIL) processes and fast-curing polymer material as the primary consumable instead of slow-curing glass.

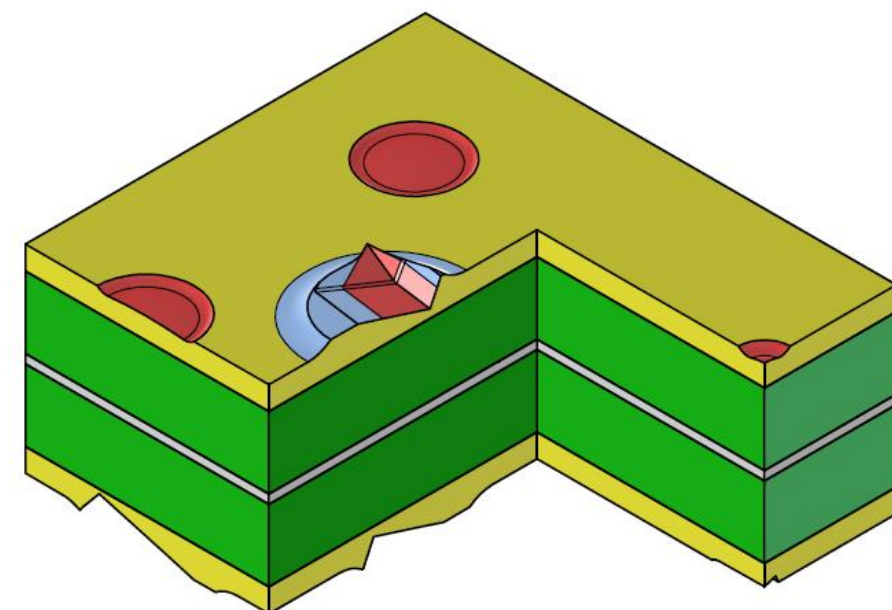
- Master replicated into polymer on glass wafers
- Hybrid master: Different optical elements on same wafer



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Step 1

Master for replication



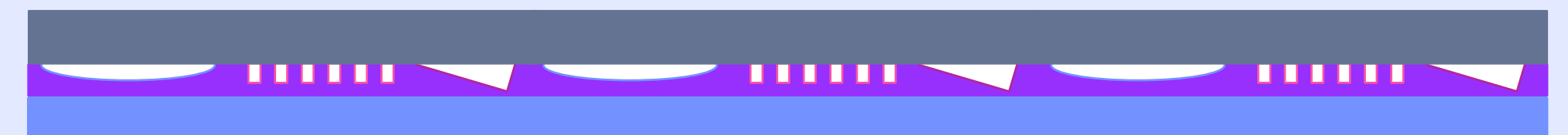
Polymer on glass wafer



UV Light



Step 2



Nano Imprint Lithography of glass wafer with polymer

Step 3

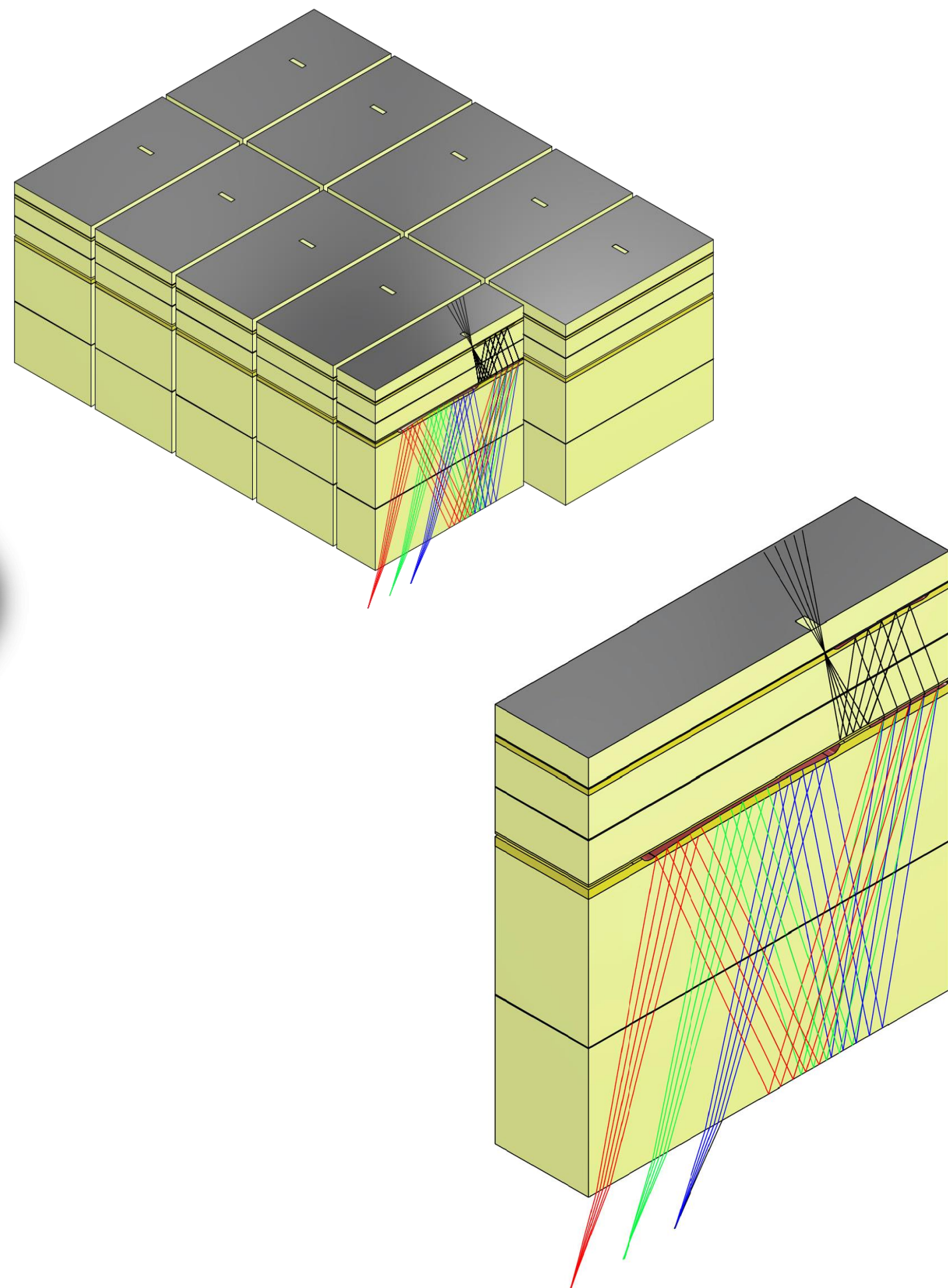


Wafer with NIL integrated optics ready for assembly

What is SPIO?

SPIO IS...

A **production technology** that allows mass production of small-size optical devices with photonic component integration.



SPIO stands for:

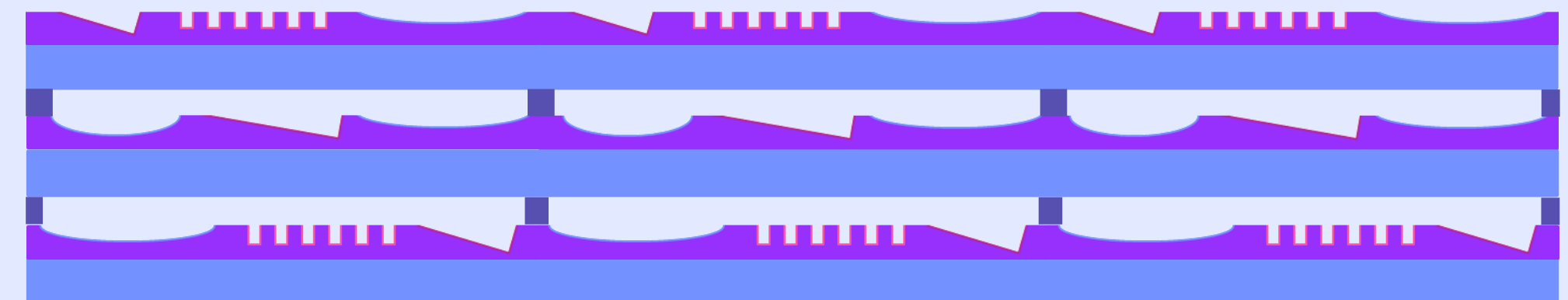


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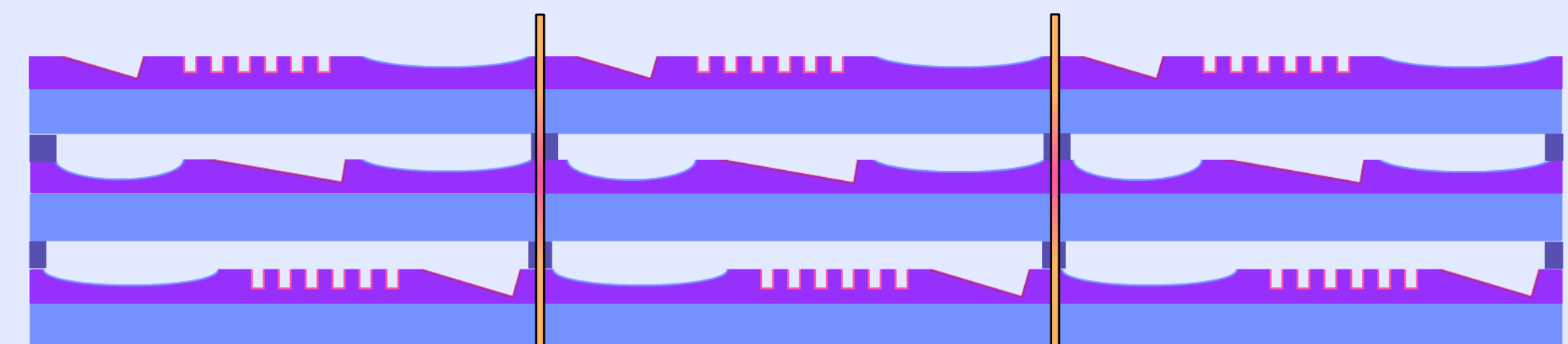
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Step 1



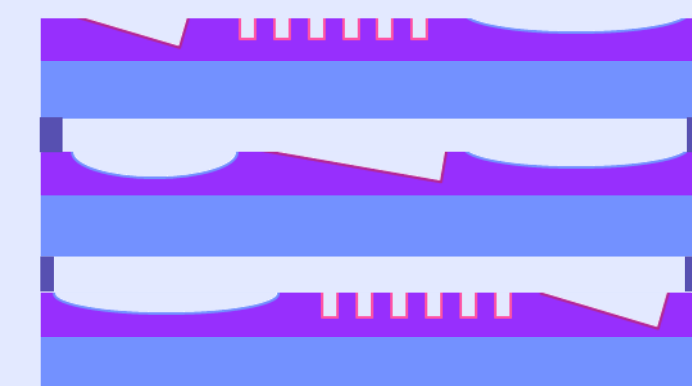
Assembly of hundreds of SPIO devices on wafer level

Step 2



Testing on wafer-level and separation into SPIO devices

Step 3



SPIO device die for integration

Summary



SPIO: Stacked Planar Integrated Optics – an optical engine production platform

OPPORTUNITIES:

- Miniaturising optical engines/devices
- New application with small formfactor optics
- Scaling rate for high volume capacity
- True hybrid optical functionality

CHALLENGES:

- Unmatured technology
- Manufacturing tolerances for high volume
- Passive alignment of wafers in assembly

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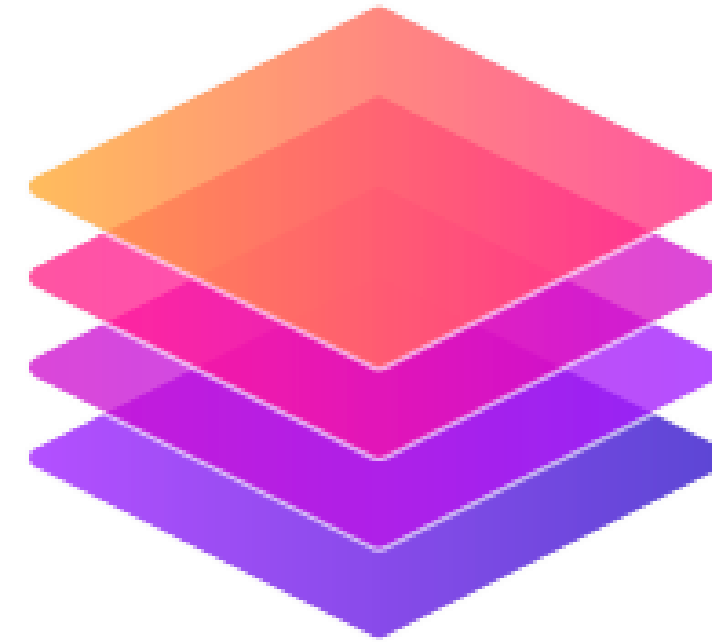
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Thank you!



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