

*Single Design, Multiple Fabs:
Modelling and Design Strategies
for Hybrid PICs*

*EPIC Online Technology Meeting
on Hybrid Photonics Integration*

Andrzej Połatyński

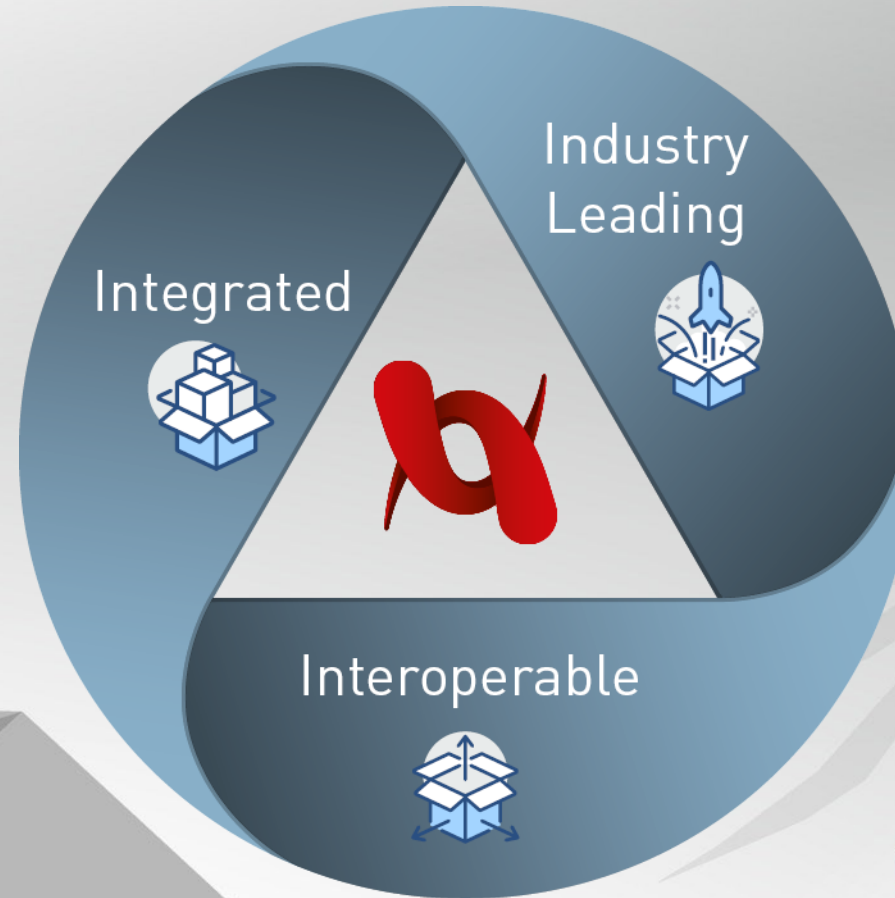
18 September 2023



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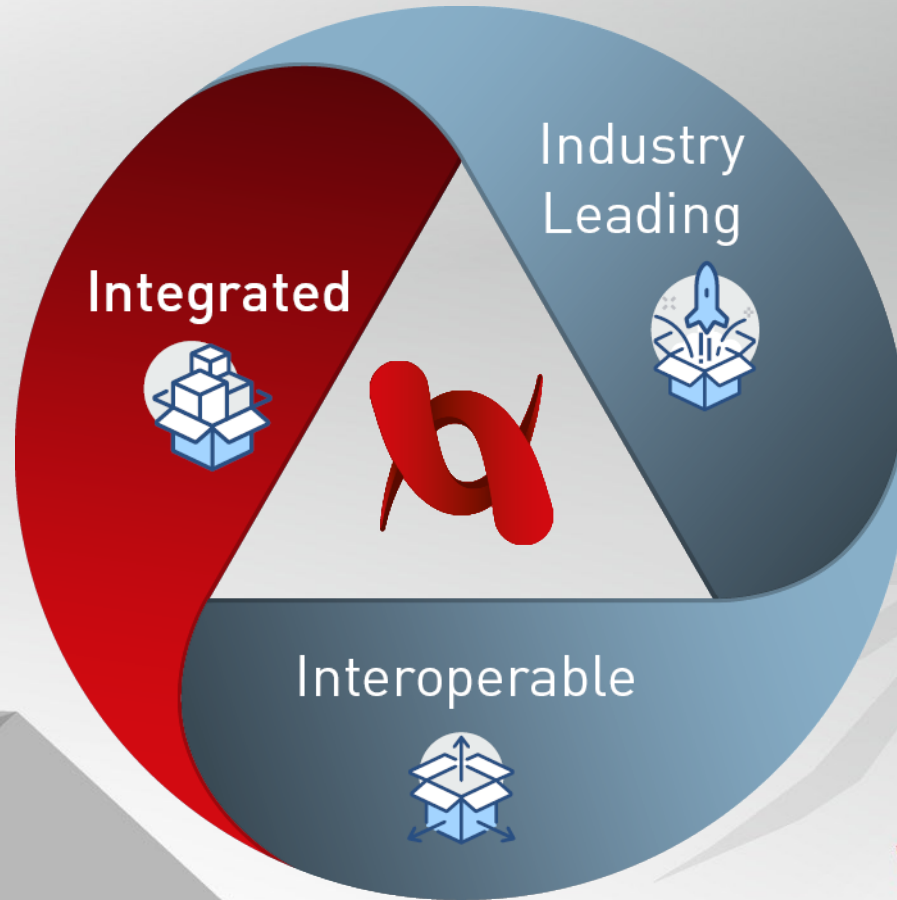


- ➔ Market leader with 25+ years of experience
- ➔ Regional offices in Europe and North America
- ➔ Global network of resellers and representatives

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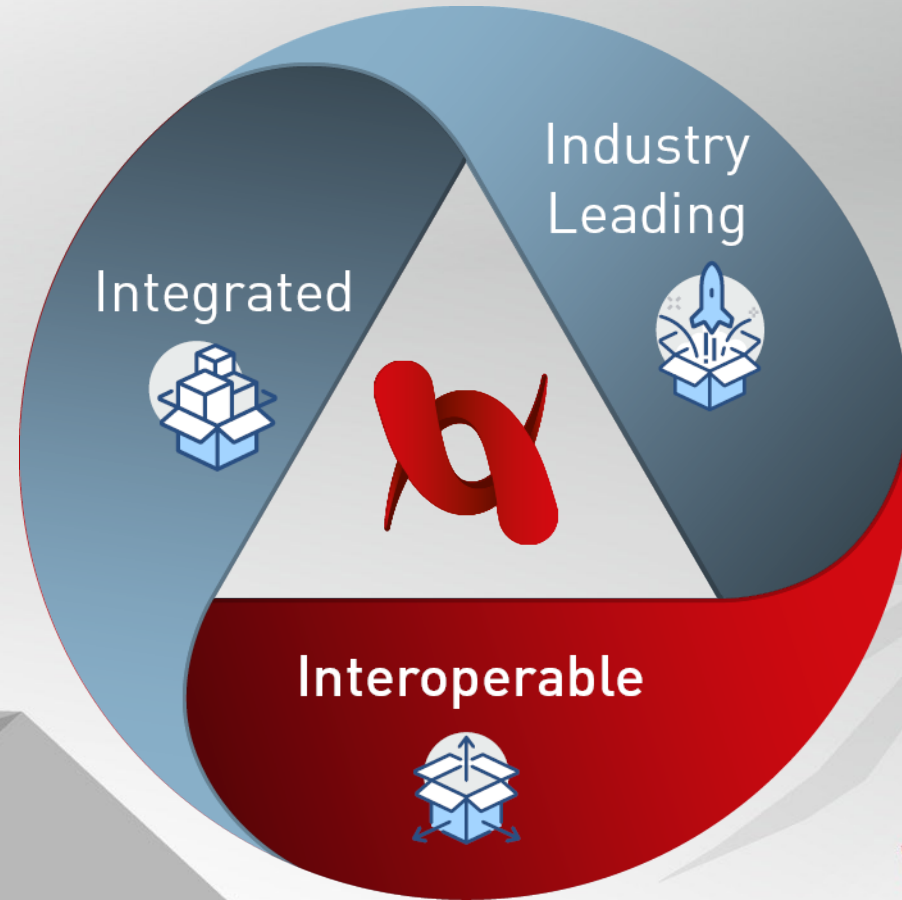


- ➔ We **integrate** efficient simulation techniques and professional design functions for devices, components and systems.

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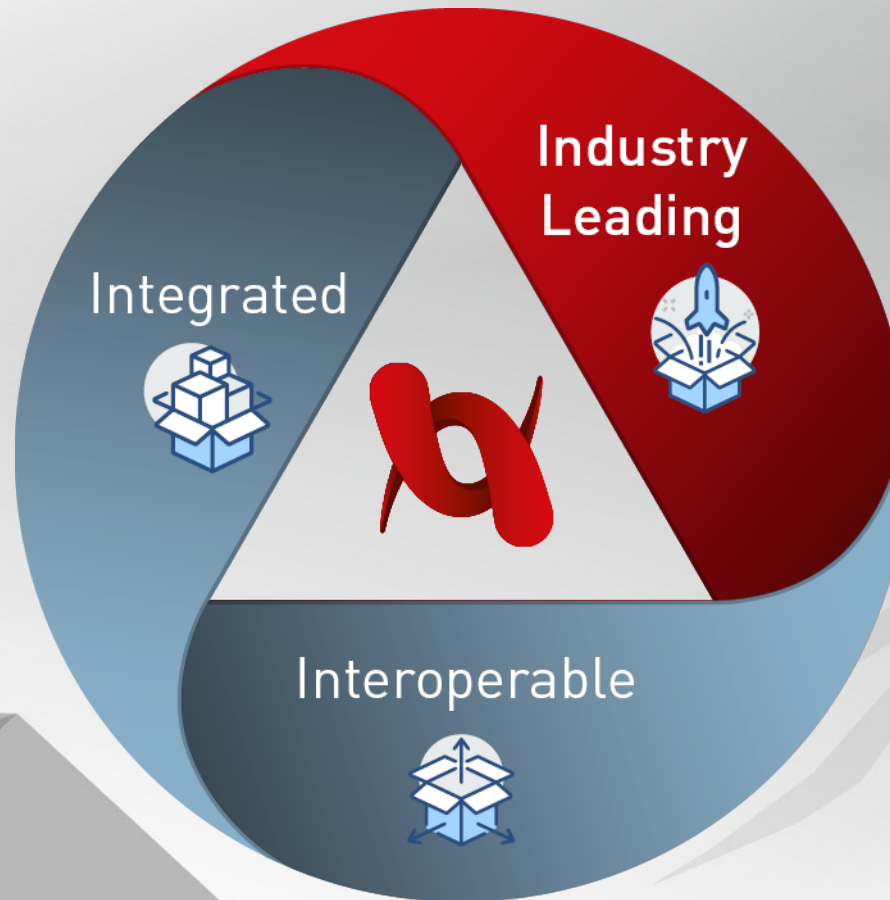


- ➔ We enable **interoperability** with 3rd party simulation, design and programming software, and test & measurement equipment.

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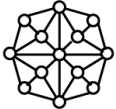
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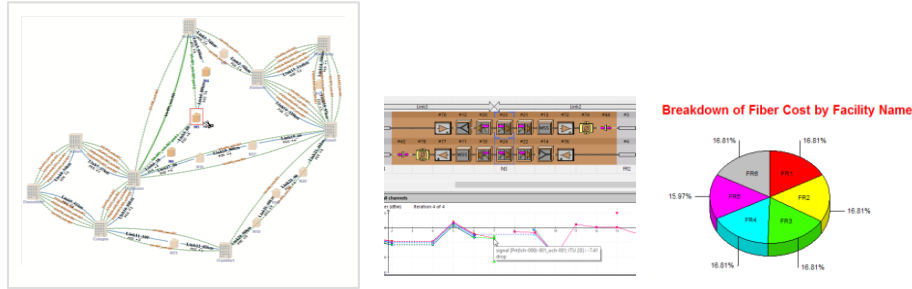


- ➔ We seek **industry leading** solutions to chart new frontiers via forward-looking industrial and research collaborations.

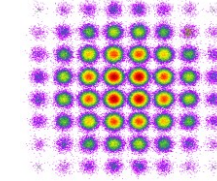
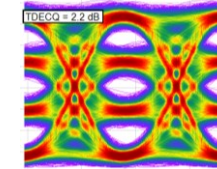
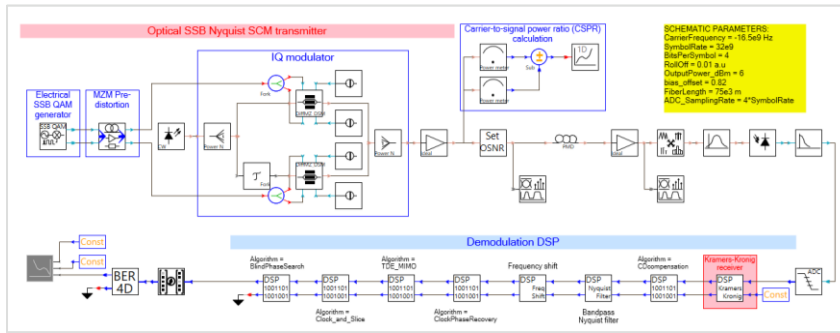

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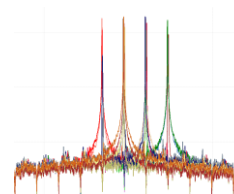
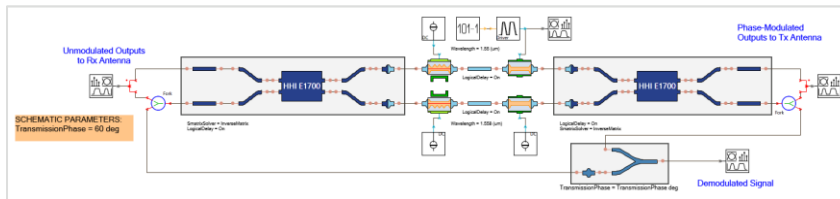
Link Engineering



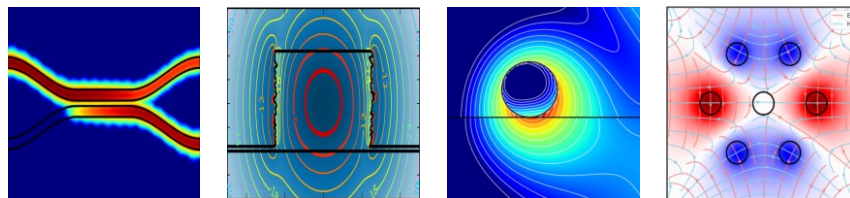

Transmission Design

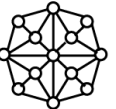
Component Design






Device Simulation




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define the cutting edge.








Link Engineering

 VPI Link Designer
  VPI Link Configurator







Transmission Design

 VPI Transmission Maker
  VPI Lab Expert
  DSP Library
  QKD




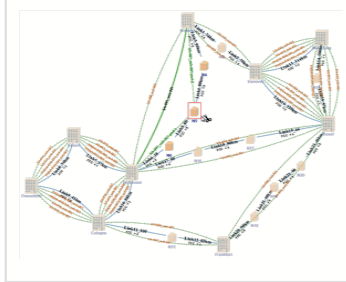
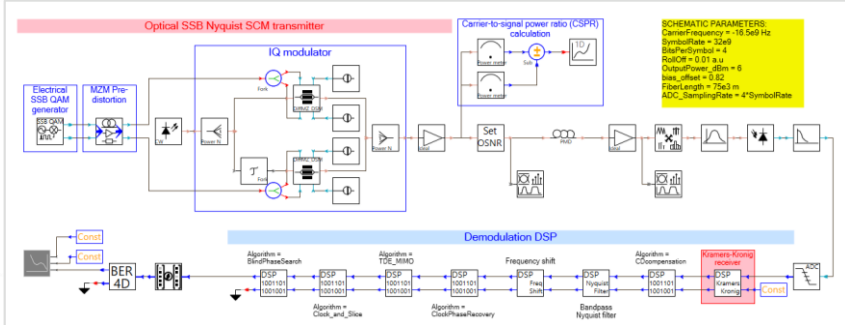
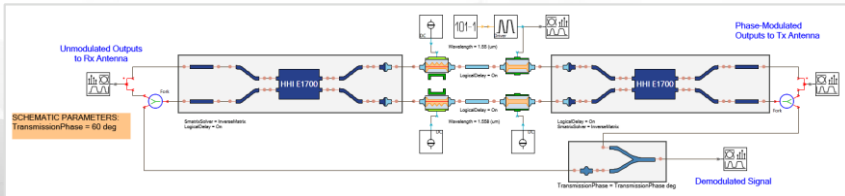
Component Design

 VPI Component Maker
Fiber Optics
  VPI Component Maker
Photonic Circuits
  PDK<fab>

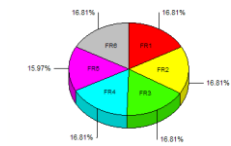


Device Simulation

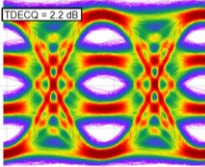
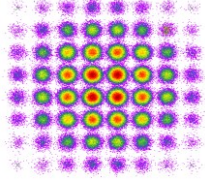
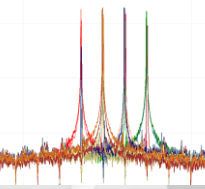
 VPI Device Designer

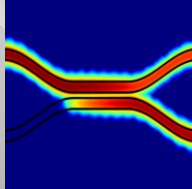
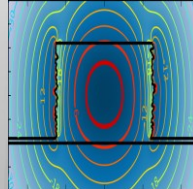
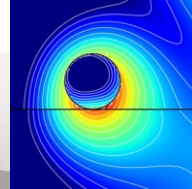
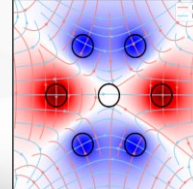




Breakdown of Fiber Cost by Facility Names



Facility Name	Percentage
FRS	15.81%
FRB	15.81%
FRD	15.81%
FRG	15.81%
FRH	15.81%
FRJ	15.81%

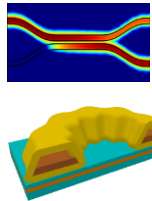




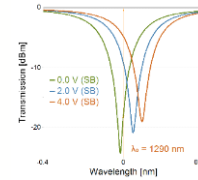


VPIphotonics Design Suite

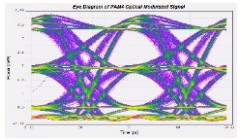
VPIdeviceDesigner



VPIcomponentMaker
Photonic Circuits



VPItransmissionMaker
Optical Systems



VPItoolkit PDK
<foundry>



Layout
Design Tools



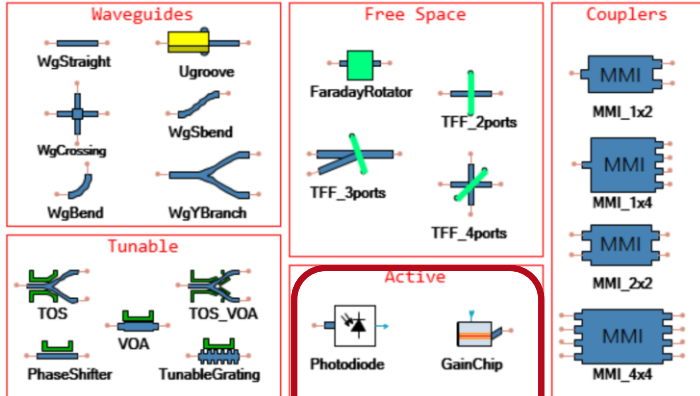
Electronic Design
Automation



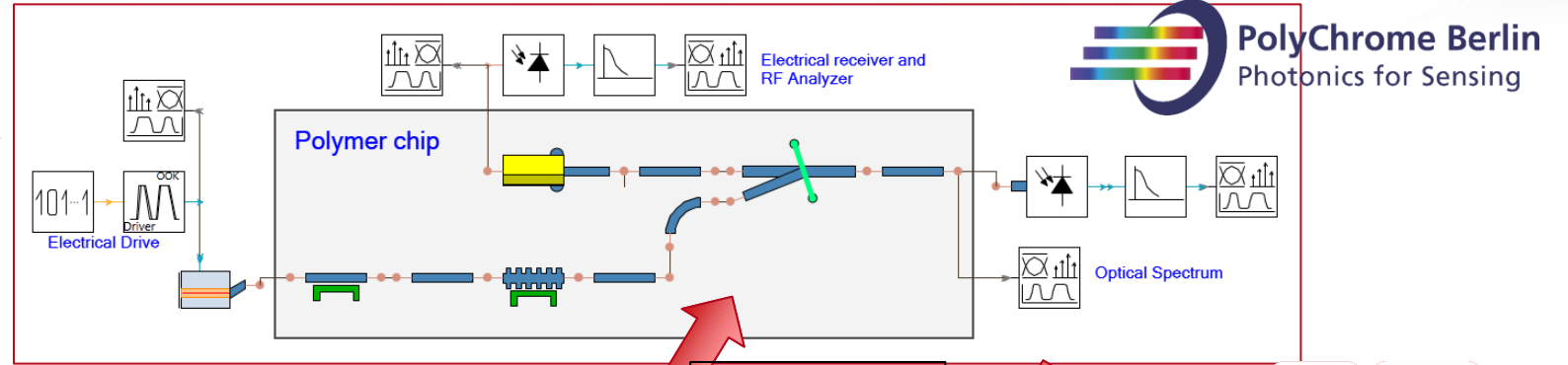
From Circuit Schematic to Assembled Chip

Polymer-based Hybrid Tunable Laser

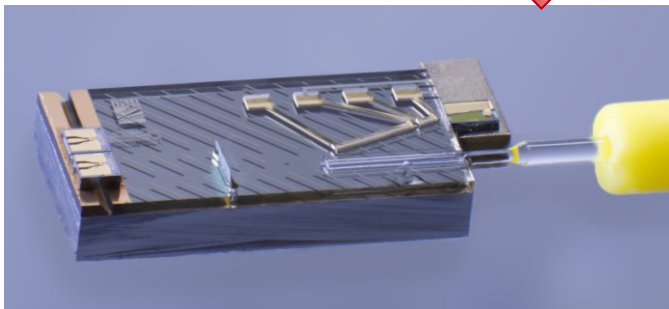
List of HHI Polymer-based Building Blocks



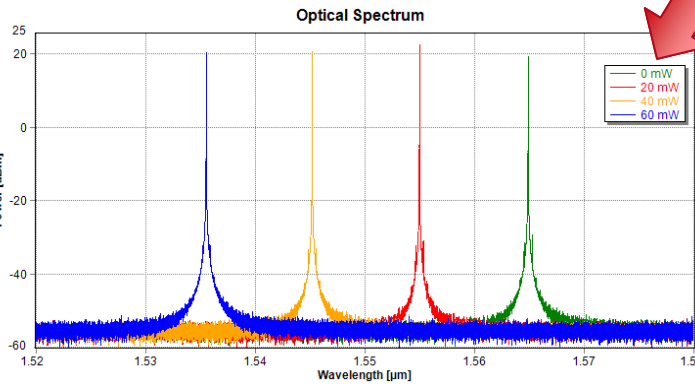
VPI circuit simulation view



Assembled Transceiver



Fabrication processes and assembly

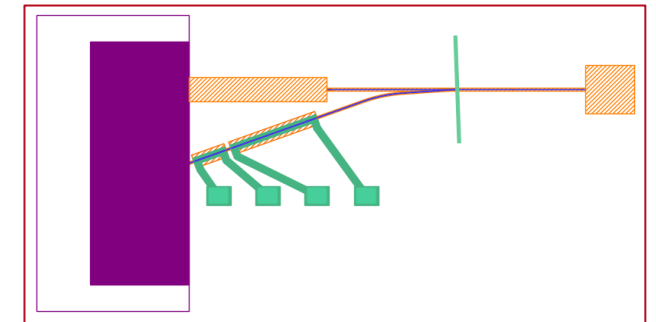


Optimization and Tolerance Simulation

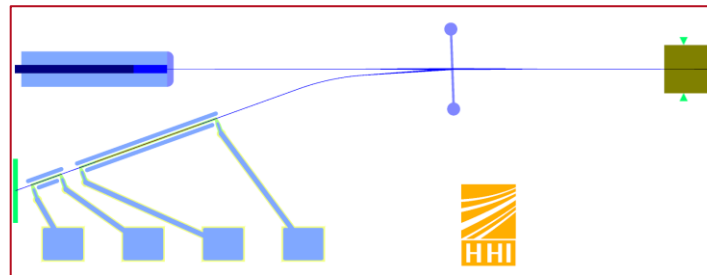
Export netlist or layout file



VPI layout view



Exported GDS (in Klayout)



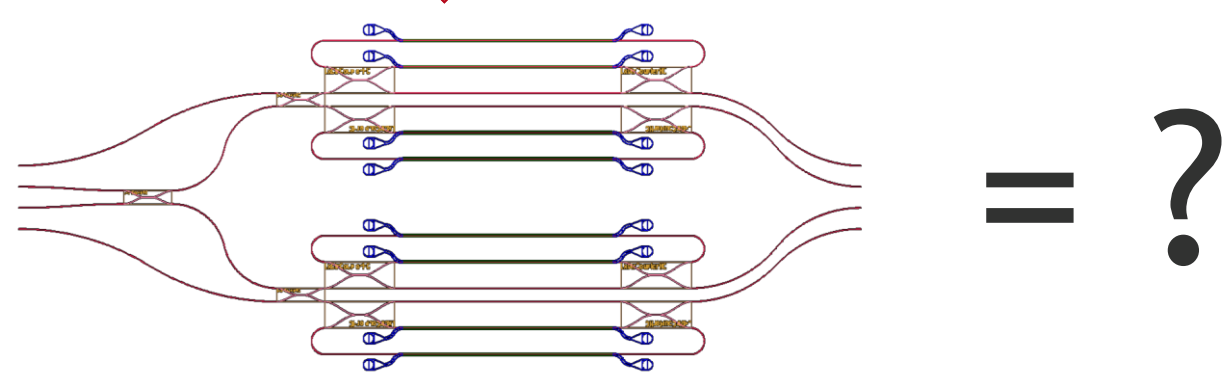
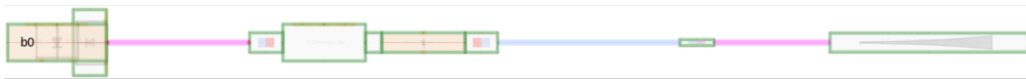
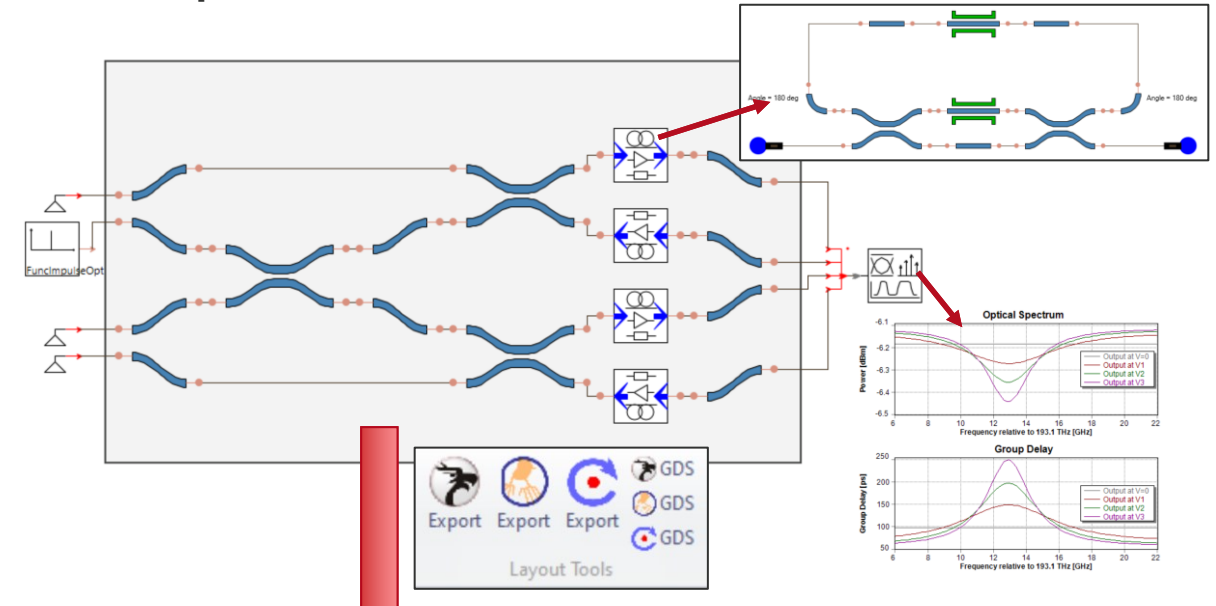
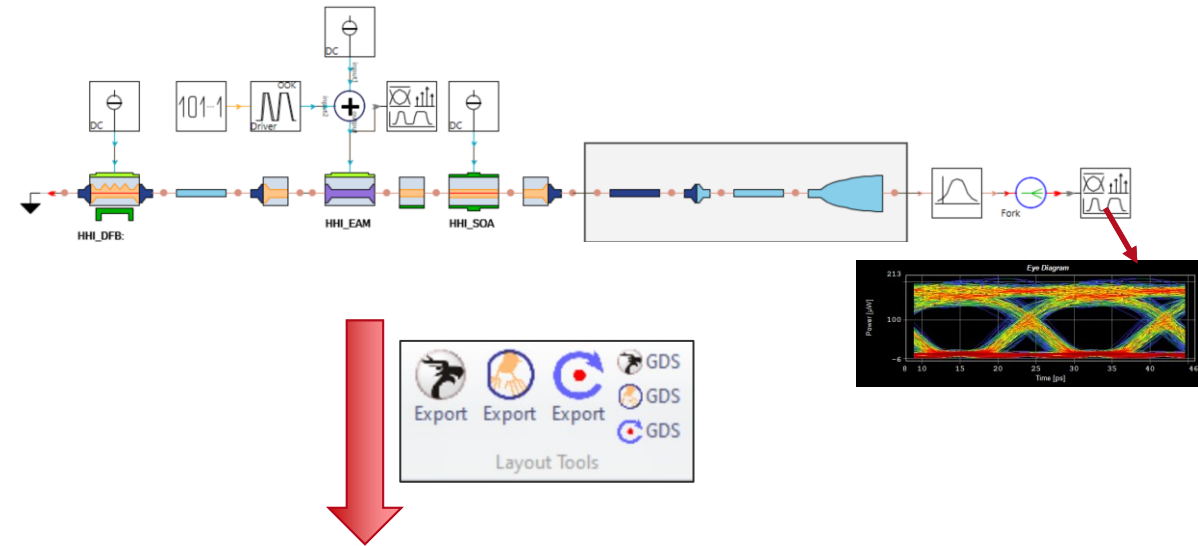
GDS extraction using given layout tool

HHI
PDK

InP Externally Modulated Laser

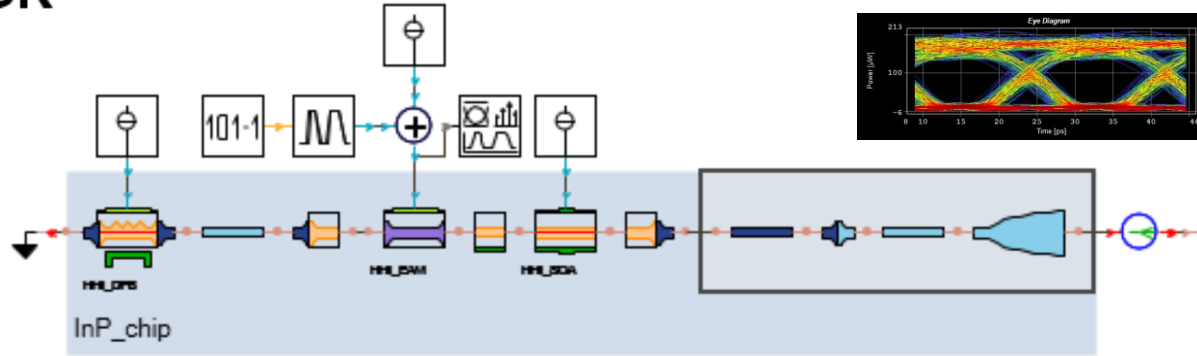
LioniX
INTERNATIONAL

SiN Optical Beam Network Former

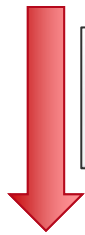
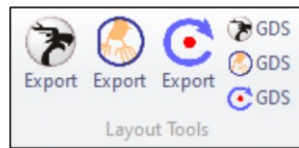


- ✓ Simplified design process
- ✓ Suitable for any foundry run
- ✗ Two layouts and two GDS files
- ✗ No possibility to verify the final performance

InP Externally Modulated Laser

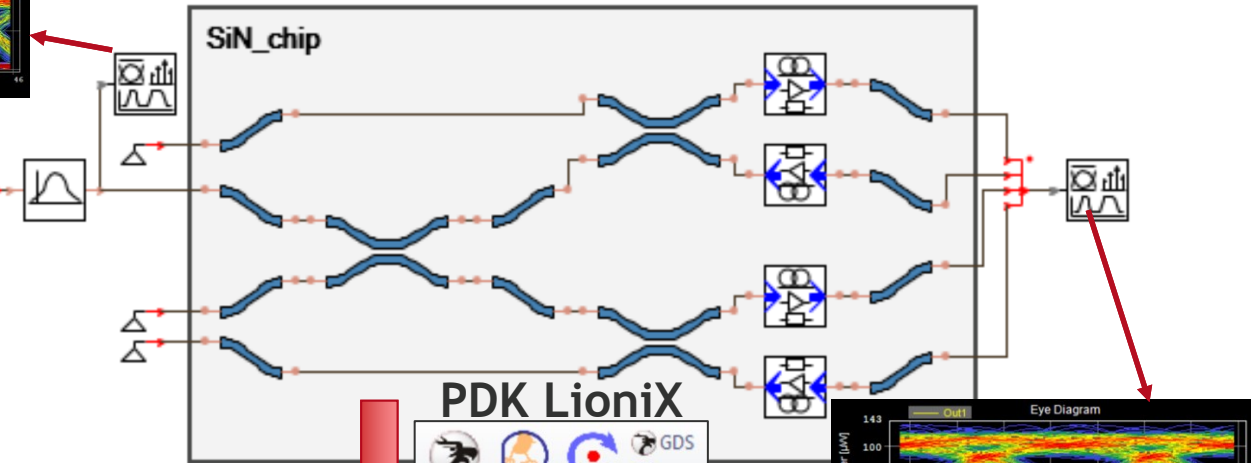


PDK HHI

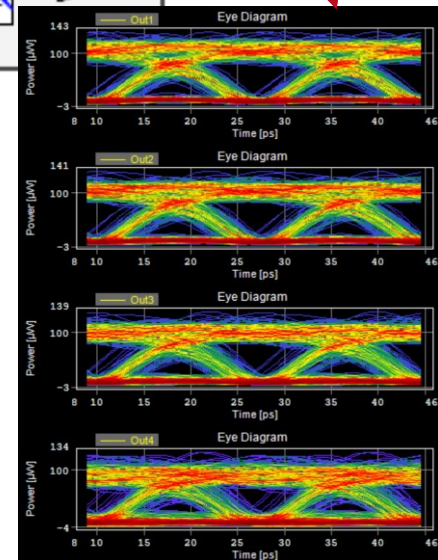
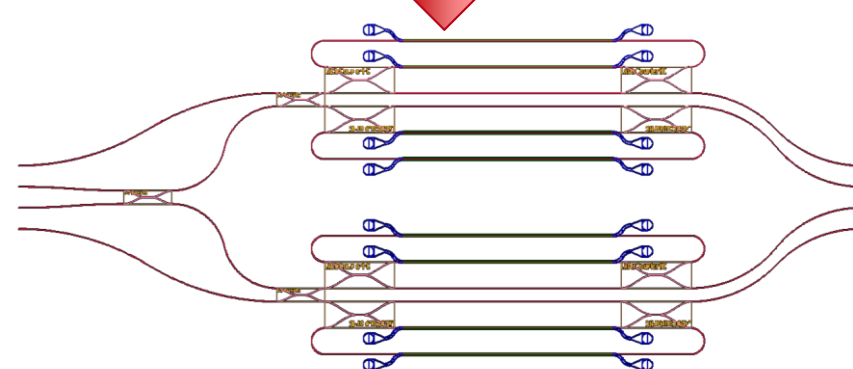
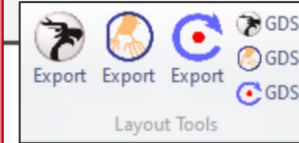


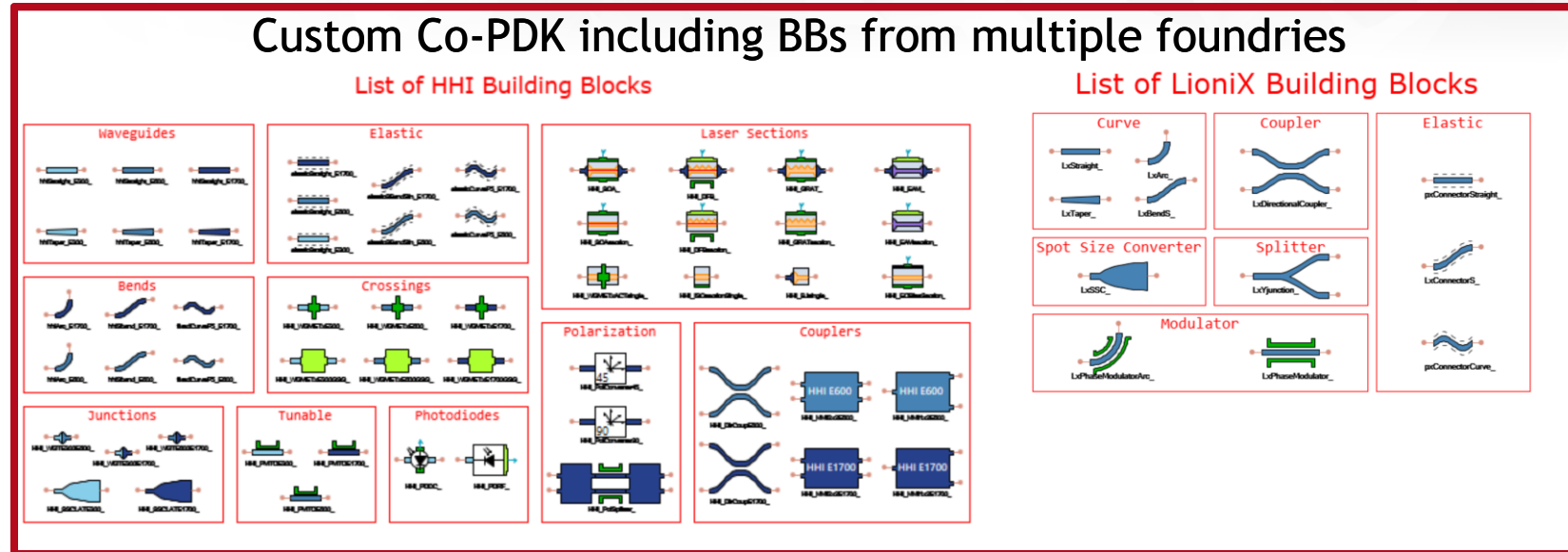
- ✓ Single design
- ✓ Optimization of final chip performance
- ✓ Suitable for any foundry run
- ✗ Two layouts and two GDS files

SiN Optical Beam Network Former



PDK LioniX

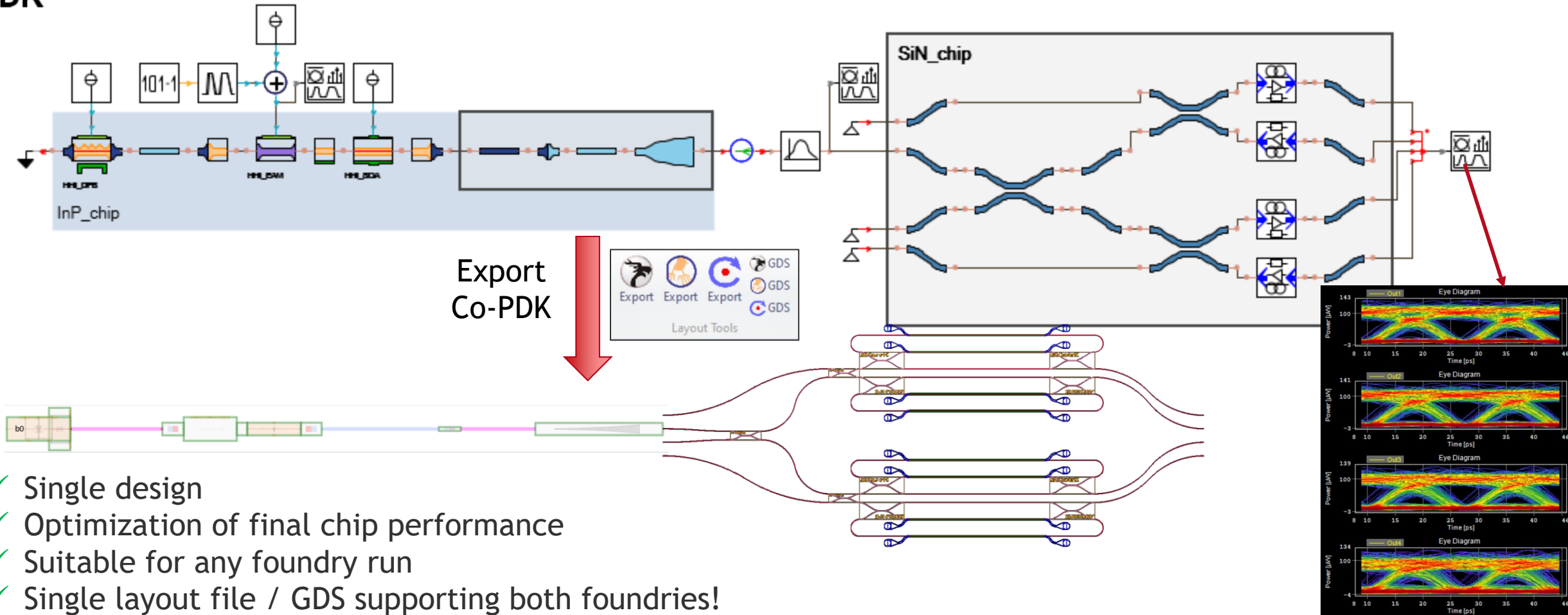




Most foundries neglect layers with non-default names!

- Create a custom PDK
- Define your components
 - by creating a custom module library
 - by merging other PDKs
- Avoid layer names overlap - if both PDKs have the same layer numbers -> rename them!
(e.g. “Layer HHI1” and “Layer LioniX1”)
- Create a mapping file for changed layer names (available in most layout tools)

Self-created Co-PDK (including HHI and LioniX BBs)



- ✓ Single design
- ✓ Optimization of final chip performance
- ✓ Suitable for any foundry run
- ✓ Single layout file / GDS supporting both foundries!
- ✗ Requires additional mapping of GDS layers (available in most layout tools) - **Standardization required!**

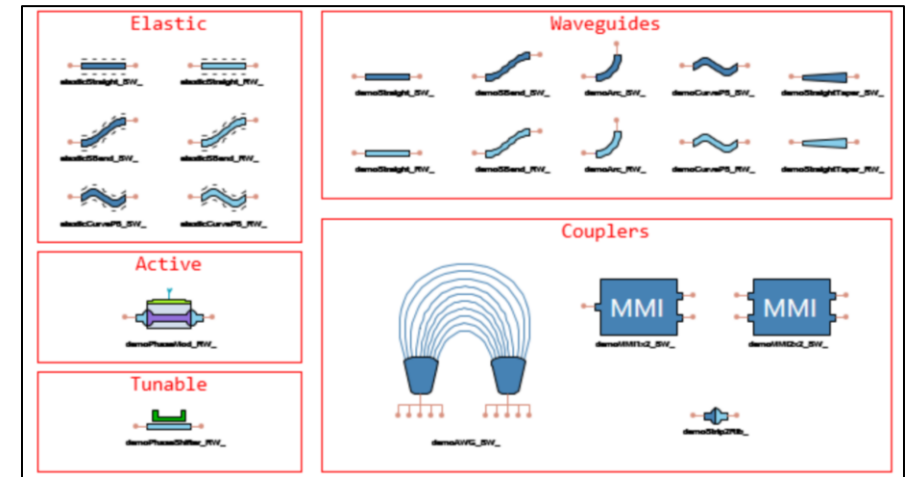
What can you do for us?

- ✓ Help in the standardization!

How can we help you?

- ✓ Enable various strategies to design hybrid PICs
- ✓ Simulate and optimize your designs
- ✓ Use bidirectional design workflow:
 - Devices ↔ Circuits ↔ Systems
- ✓ Custom PDK integration framework
 - ⇒ Easy to build, use, and share PDKs
- ✓ Design acceleration via automated export of the layout

Build your PDK with us!



Contact us to learn more about:

- *Simulation and Optimization of PICs*
- *Design workflow from Devices to Systems*
- *Support of fabrication tolerances*
- *Schematic-driven layout-aware design methodology*
- *Electronic/Photonic Co-Simulation*
- *Design of custom PDKs*

Contact Us!

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