# Plasma - Therm

ELECTRONICS AND PHOTONICS: OVERLAPPING PLASMA PROCESSING REQUIREMENTS

November/2023



# Company introduction

- End users' applications and Plasma processes requirements and solutions
  - Etch
  - Deposition
- Plasma process use case for hybrid package solution
  Conclusion

## SOLUTION - SPECIALTY MARKETS





# Etch

ICP – High density plasma RIE – Low density plasma DSE<sup>TM</sup> – Deep Reactive Ion Etch IBE – Ion Beam Etch ALE – Atomic Layer Etching HDRF – Remote plasma



# Deposition

PECVD, HDPCVD, LAPECVD PVD - Sputtering IBD - Ion Beam Dep F.A.S.T. - Fast Atomic Sequential Technology



# Thermal Processing

RTP – Anneal, Oxidation, Activation, Silicides



# Dicing

PDOT – Plasma Dicing on Tape PDOC – Plasma Dicing on Carrier PDBG – Plasma Dicing Before Grind Photonics AR/VR and Displays. µLEDs VCELS & Optoelectronics





Wireless & Connectivity 5G, RF Filters, BAW, SAW, BAR, PA, IoT, Mobility

#### Semiconductor & Advanced Packaging Logic & Memory,





## **Power Devices** Power Electronics, Wide-Ban

Power Electronics, Wide-Bandgap Energy & Battery Technology

> Memory & Storage HDD. MRAM & NVM Big Data & Edge

IDM, Foundry, OSAT





Sensors & MEMS Image Sensors, LiDAR, Actuators. Life Sciences

#### Scientific & R&D Universities, Research Institutions, Governments



CONFIDENTIAL

plasmatherm.com

#### RANGE OF APPLICATIONS → RANGE OF PROCESSES

# Plasma • Therm

# Applications/Devices

# Photonics

- Lasers
- Gratings
- Micro-lenses
- Optics
- Photonic crystals

124-918 B

- Detectors
- VCSELs
- LEDs
- Solar cells
- Electronics
  - HBTs
  - HEMTs
  - Diodes



# Processes

- Deposition:
  - Hardmasks for etching (e.g., InP)
  - Waveguide cladding
  - Hydrophobic barrier layers
  - Bragg mirrors (e.g., TiOx/SiOx
  - Transparent conductors (e.g., ZnO)
  - Contacts Ohmics, Shottk
  - Conformal
  - Passivation
- Etching
  - Profile control (sloped, vertical)
  - Damage considerations
  - Slow and fast rates
  - Selectivity between layers

### PLASMA ETCHING PROCESS SOLUTIONS



0

MACR

10

N0

NA

MO

F R

TECHNOLOGY



> 5 to 500 um features



0.1um to 5 um features



<0.1 um features

Structures: nanoscale to macro, sloped to vertical Gratings to vias and micro-lenses

Materials: spanning wide property range III-Vs, SiC, Si, metals, dielectrics, DLC, piezo materials

Processes ranging from slow, controllable, low damage to high rate Endpoint, cleaning, stripping, ion beam, wide temperature ranges



🖻 Plasma-Therm



#### INTEGRATING MULTIPLE COMPATIBLE TECHNOLOGIES



VERSALINE ICP Technology Dielectric etching, Metals Compound semiconductors



IBE Technology Metals, Piezo Materials



4-, 6- or 8-Sided Cluster Systems Cassette-to-Cassette







### InP Based Lasers & Modulators

- Vertical profiles
- High aspect ratio
- Smooth sidewalls for low loss

# (Requires high temperatures and hardmask)





### **Profile Control**

- Selectivity control
- Sloped profiles
- Smooth for low loss



## Material Range

- Selectivity between materials
- Low damage
- Controllable rate
- Controllable profiles







Slow, Controllable





Lowpower ICP operation with multi-turn coil





#### CONFIDENTIAL

lasma Cherm

🖥 Plasma-Therm

1 μm features InGaP/ AlInGaP Etch on GaAs

## PROCESS CONTROL: SELECTIVITY, RATE, PROFILE







- ZnO Transparent Conductive Layer
  - Fast Atomic Sequential Technology F.A.S.T.
  - Ideal for thick and conformal layers
  - High deposition rate





Signal A = InLens

Grand. = 108.32 K X

- Dielectrics with PECVD and HDPCVD
  - Index control
  - Surface passivation
  - Stress control
  - High deposition rate





Transceiver Example



 Challenge: Complicated and multiple materials (PCB, thermoplastics, epoxies, metal, SMT components, optical fibers)





■ Test vehicle: FireFly<sup>™</sup> an on-board high-speed data communications optical transceiver

- Applications: mil/aerospace, automated test equipment, ASIC, supercomputing, Industrial & medical
- Requirements
  - IPC Class 3
  - Conformal Coating
  - Resistant to Fretting
  - Vibration Tolerant
  - Salt Fog Protection

# Performance:

 No degradation in optical power after 48 hours salt fog exposure

Courtesy of

- Extreme Humidity
- Sand and Dirt Protection
- Extreme Temperature Operation
- Tin Whisker Mitigation
- Resistant to Fungus









Phononics applications have become mainstream thanks to advanced packaging / more than Moore industry trend

- Plasma processing (etching, deposition, surface preparation/cleaning, plasma dicing) developed for various mainstream applications benefit to Photonics industry
- We have global interaction with microelectronic industry and are here to help with
  - Research projects (internal and collaborative)
  - Solutions from academic to volume production

# How can we help you?

Web: plasmatherm.com Email: sales@plasmatherm.com

# in 🕑 🖸 f

# Plasna-Them

00





# Partnership – Excellence – Innovation

### WHERE WE ARE



#### PARTNERSHIP



It's What We Do



# 11 Consecutive Years

# RANKED 1<sup>st</sup> Etch & Clean Equipment Supplier



# 🖬 Plasma-Therm







Cassette to Cassette & Cluster Configuration Systems Single Substrate and Load Lock Systems

Bench-top Single Substrate

Process Tools VERSALINE, Singulator, QuaZar, Kobus, Endeavor, Eclipse, Heatpulse, Tegal, Odyssey, Mask Etcher **Process Tools** SHUTTLELINE, Takachi, Kayen Process Tools Vision PlasmaPODS