

Photonic Boards: Opportunities and Challenges of Electro-Optical Co-Engineering

Dr. Nikolaus Flöry, vario-optics ag



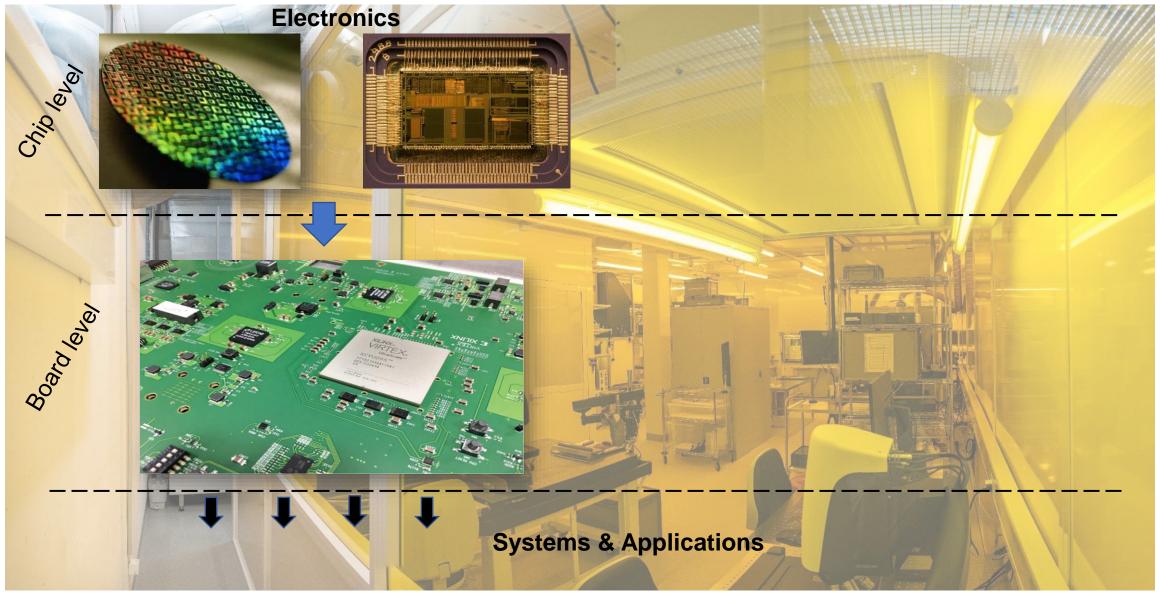
Who we are





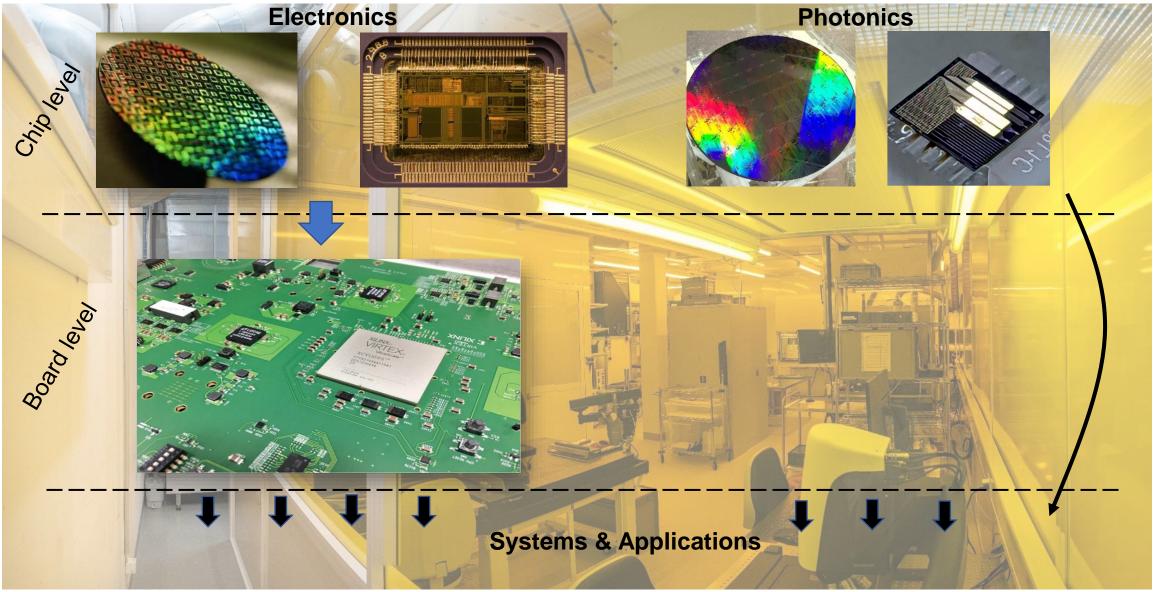
Photonic Boards?





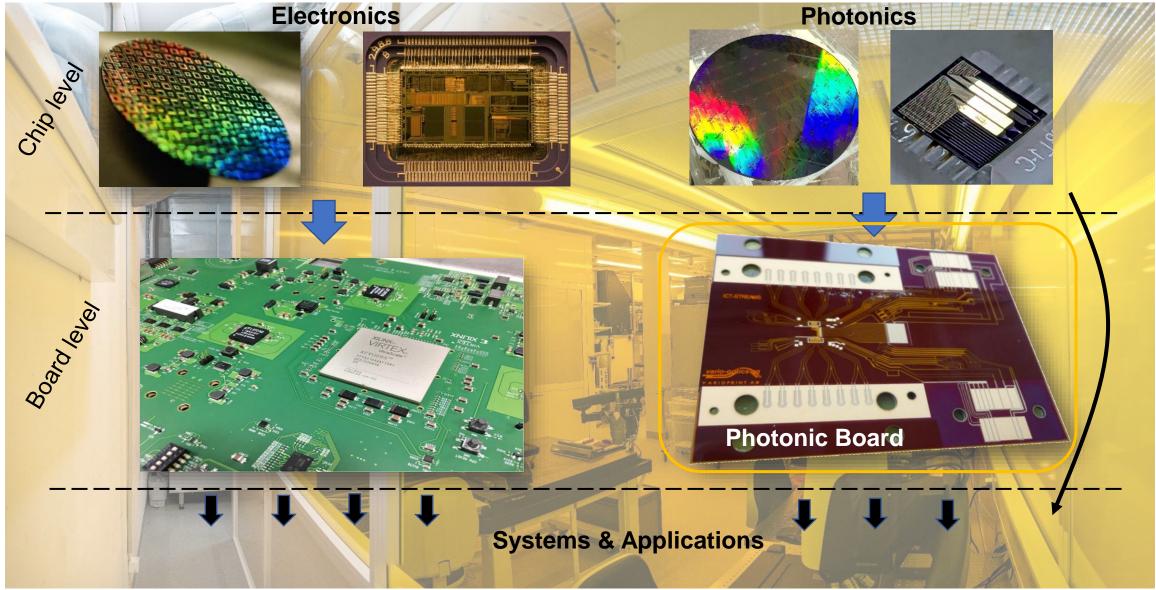
Photonic Boards?





Photonic Boards?

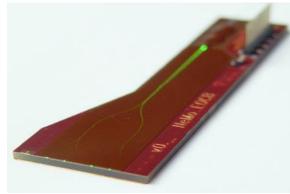


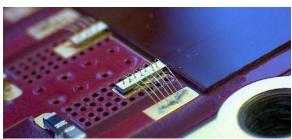


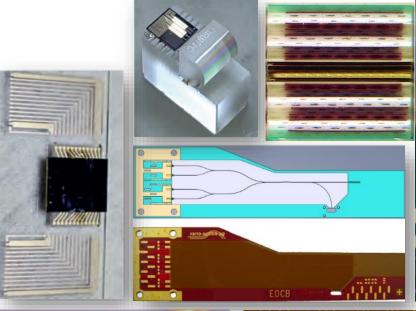


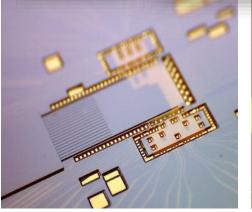
Photonic Boards

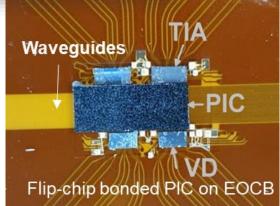
- optical
- electrical
- mechanical
- thermal

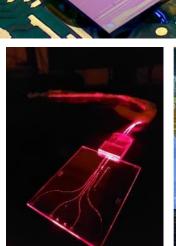








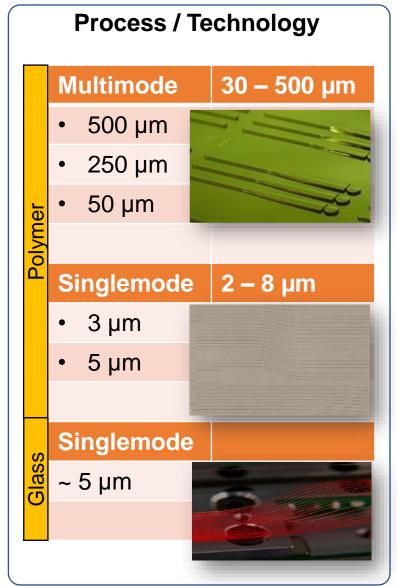


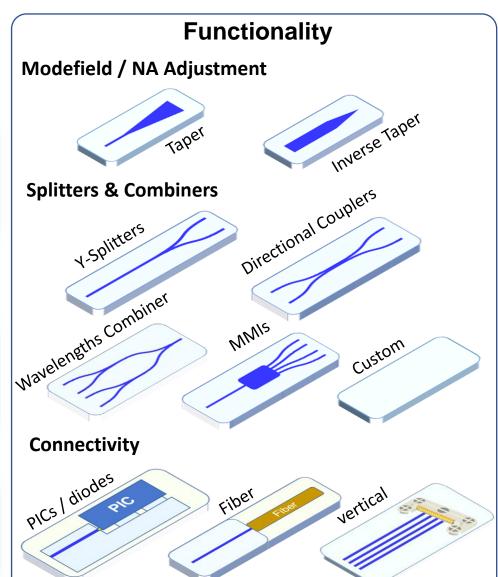


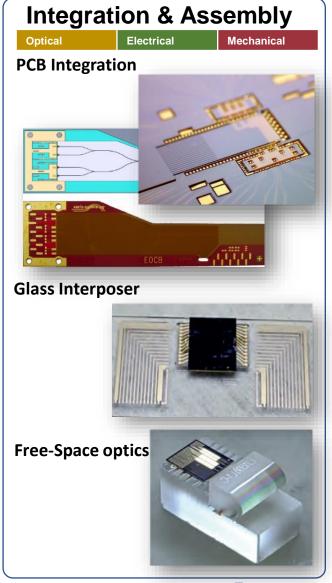


vario-optics Waveguide Technology Portfolio > 15 years of Waveguide R&D Know-How





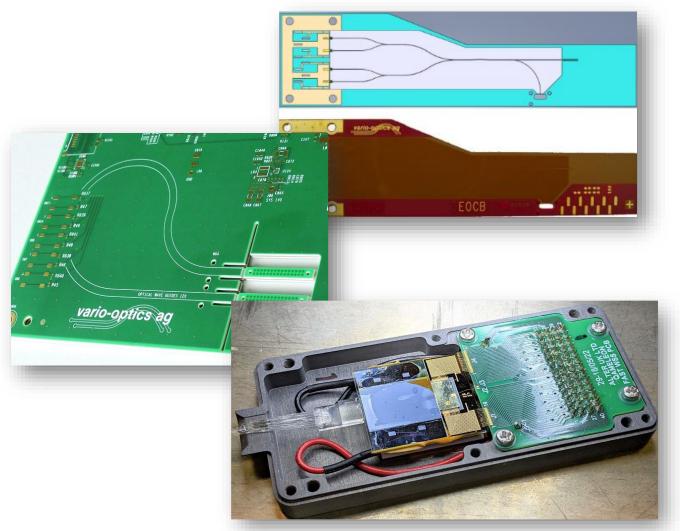






Requirements for good/advanced PIC packaging

- Co-engineering!
 - Consider packaging before PIC fabrication
- Multi-disciplinary approach
 - Many PICs also have high electrical & thermal requirements (RF etc)
- Scalable approach
 - Packaging effort should not increase with port count (e.g. minimize use of fibers)



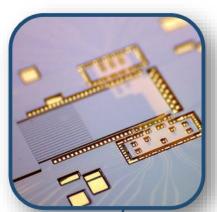
Vario-optic's PIC Packaging Platform





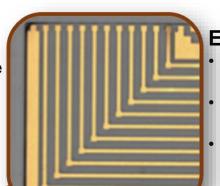


- High I/O number optical Fan-outs
- On-chip mode conversion
- Polarization maintaining Waveguides



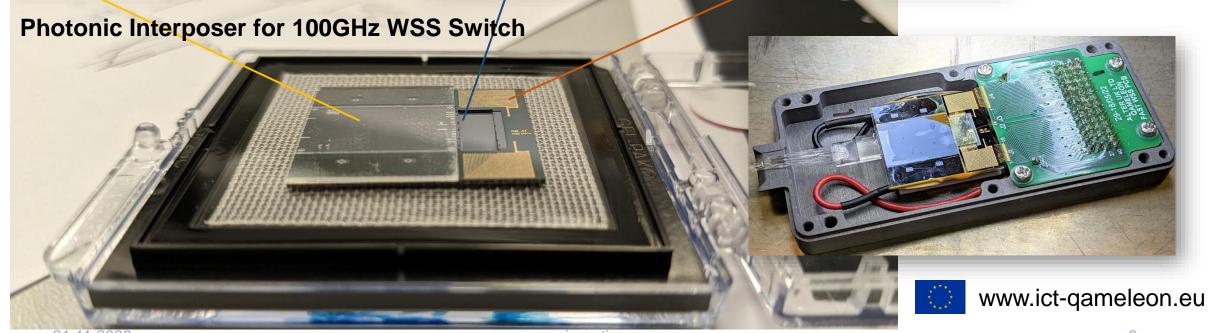
Optical Interfaces

- Efficient PIC-Waveguide Coupling
- (Adiabatic or Butt-Coupling)
- Fiber-Interface & Connectors



Electrical Interface

- Metallization & PCB Integration
- Fine-Pitch, Flip-Chip
 Bonding
- RF (> 100 GHz)



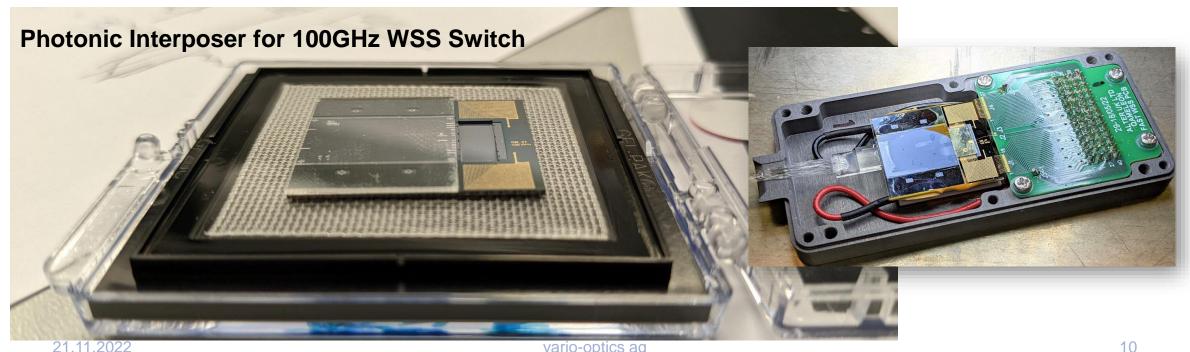
Vario-optic's PIC Packaging Platform based on EOCBs



We are always looking for partners/collaborations which can make use of an advanced PIC packaging platform.

We can provide (optical/electrical/mechanical) subsystems, which can enable high-speed, high port-count & scalable photonic applications.

Get in contact -> n.floery@vario-optics.ch





The Future is Bright!

