# **1. COMPANY OVERVIEW**



## **OPTICAL SOC TECHNOLOGY**

#### Our optical SoC (System-on-Chip) technology offers low-cost DWDM and tunability at the network edge by:

- Monolithically integrating all optical system functions
  onto a single chip
- Using non-hermetic packaging technology to scale volume cost-effectively
- Integrating complete module solutions in many standard form factors





#### FROM CHIP TO MODULE

- Easy to integrate InP monolithic chip into cost-effective photonics modules
- Technology platform for easy scaling to large volumes
- Proprietary packaging and assembly for significant cost advantages



2", 3" or 4" InP Wafer



PIC s – Monolithic Tunable Transmitter



Optical Sub-Assembly (OSA, TOSA, ROSA)



Module Assembly Optical and Electronic





## WHERE IS THE FLEX?

#### MECHANICAL DECOUPLING IS A MANUFACTURING BOTTLENECK



#### **TRANSCEIVERS: CAUGHT BETWEEN A ROCK AND A HARD PLACE**

But still carrying all your data!

PHOTONICS



Photonics

EFFECT

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### **INTEL APPROACH: OPTICAL FLEX**

Electronics are fixed, and the fiber takes up the flex

#### By fixing the electronics to the outside world:

- PRO: High quality, well controlled electrical path, volume electronics manufacturing
- CON: Fiber management in a small formfactor is very manual, costly and non scaleable



Source: Yole/Systemplus



### **EFFECT APPROACH: ELECTRICAL FLEX**



#### By fixing the Optics to the outside world:

- PRO: very low cost, very scaleable optical alignment, easy module assembly
- CON: High density flexible RF and DC interfaces are needed, electrical limitations on how many signals can go across flexible domain



## THERE HAS TO BE A BETTER WAY THAN GOING BACK AND FORTH



FECT Photonics is interested in scaleable and automatable packaging and testin solutions that fix the "flexibility" problem



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