iPRONICS

Programmable Photonics

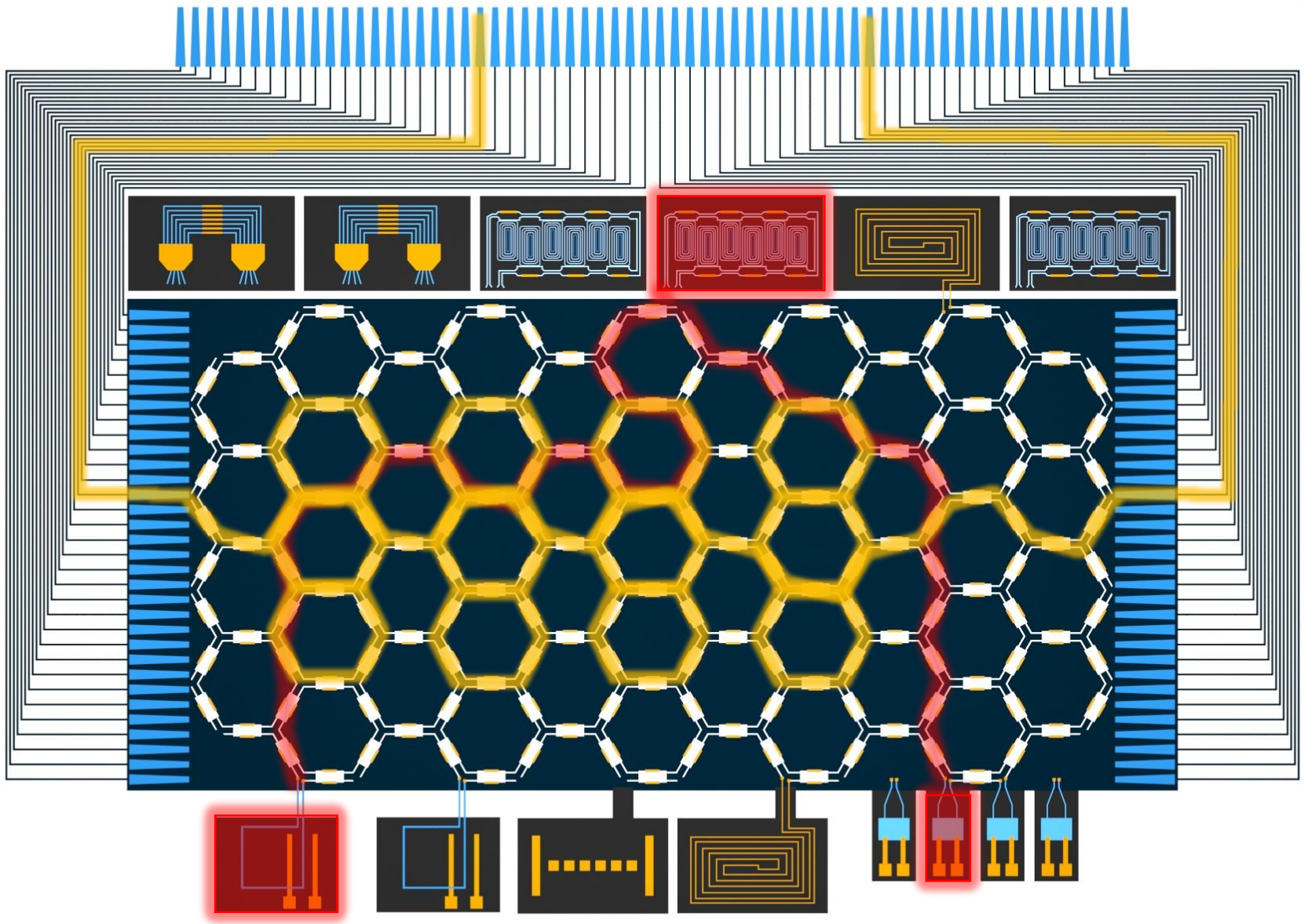
Packaging challenges on Programmable Integrated Photonics

Ana González
Director of Strategic Partnerships


IoT
FPPGA
iPRONICS
iPRONICS
Programmable Photonics

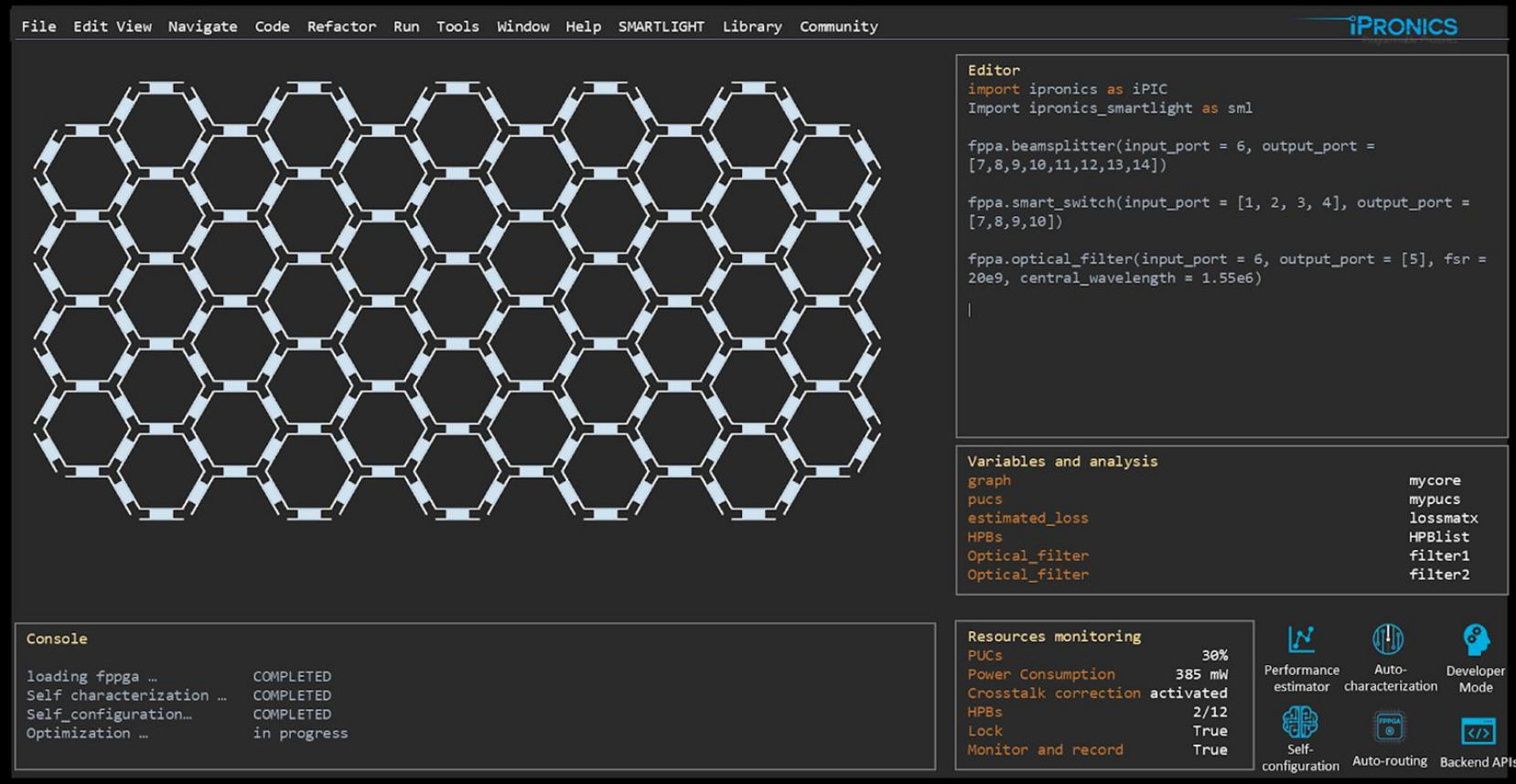
iPronics FPPGA

iPronics has developed the hardware of a multipurpose photonic processor including a flexible optical core and IP/High performance blocks



iPronics SMARTLIGHT

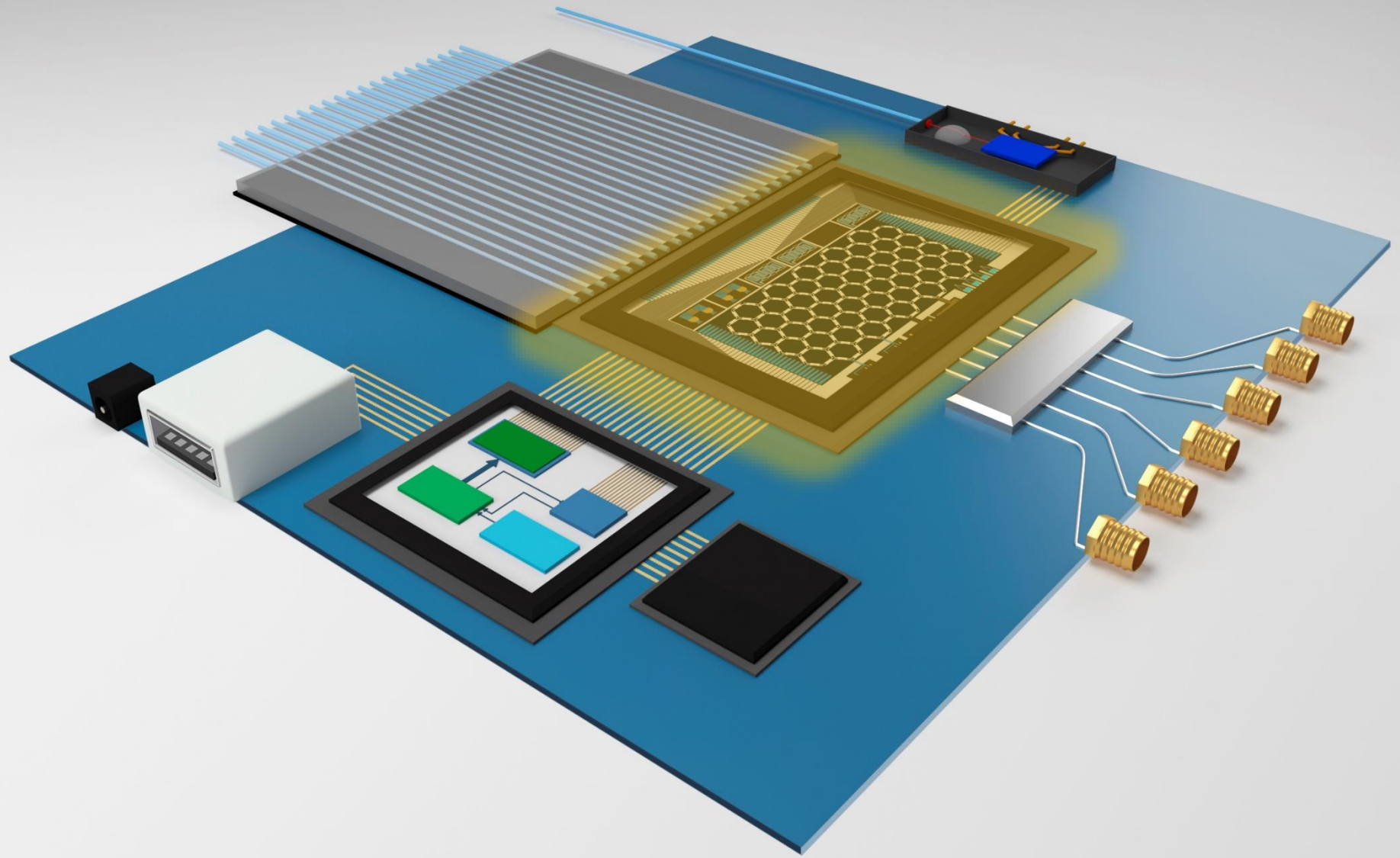
iPronics has developed the necessary software to program, control and optimize our solutions in a user-friendly yet, powerful way.



iPronics FPPGA

iPronics has developed the hardware of a multipurpose photonic processor including a flexible optical core and IP/High performance blocks

iPRONICS
Programmable Photonics



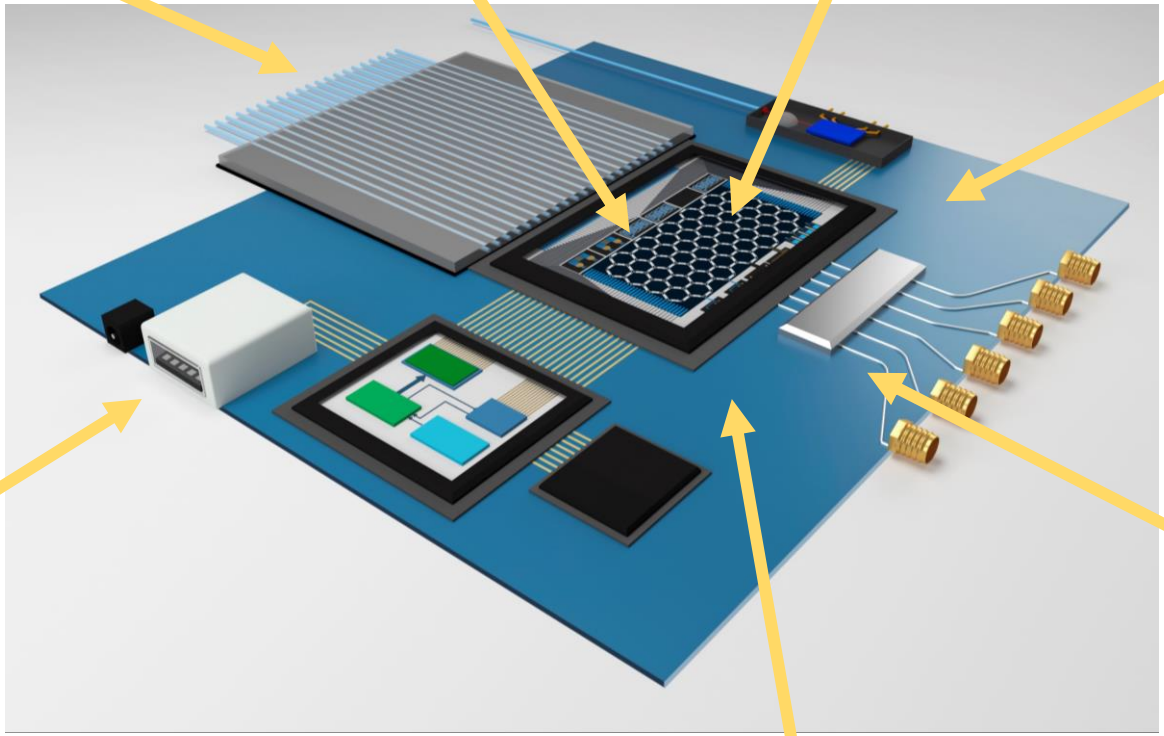
Pic design:

- Chip level
- System level

Fiber Arrays (32/64/128)

Silicon foundries (testing?)

PCB



- Electrical substrate manufacturing
- Multi Electronic Driver Array
  - 2000 channels
  - 10mW per channel
- Multi Electronic Monitor Array
  - Readout PD > 500 monitors
  - Time response: 10 ms

Packaging services

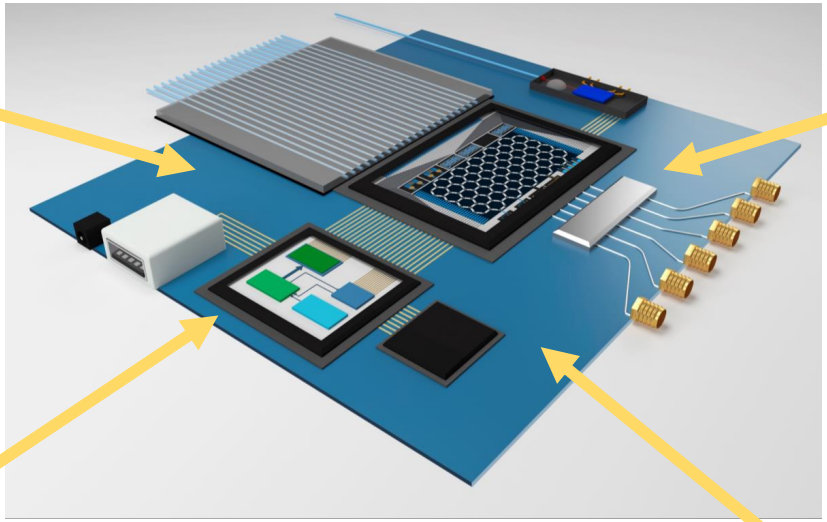
- Flip chip bonding
- Wire bonding
- Fiber Align
- Testing

TEC

Apply to our β-tester program

Data Center Applications

RF Applications



General Purpose

Computing

iPRONICS

Programmable Photonics

Packaging challenges on Programmable Integrated Photonics

Ana González
Director of Strategic Partnerships