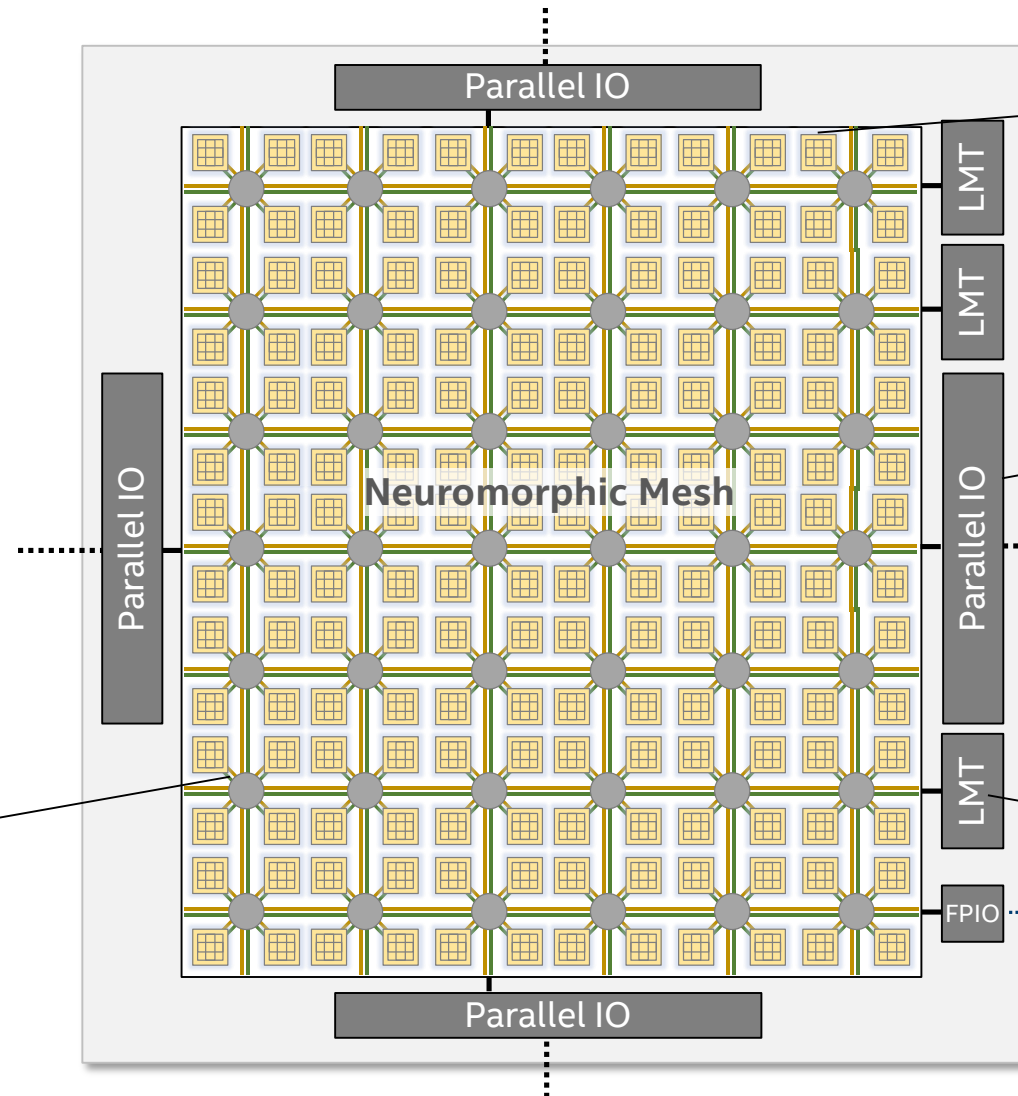


intel®

# Intel's Neuromorphic Research Chip Loihi

Technology:	14nm
Die Area:	60 mm <sup>2</sup>
Neuro cores:	128 cores
x86 cores:	3 LMT cores
Max # neurons:	128K neurons
Max # synapses:	128M synapses
Transistors:	2.07 billion
Memory:	33 MB



## Neuromorphic core

- Leaky integrate-and-fire neuron model
- Programmable learning
- 128 KB synaptic memory
- Up to 1,024 neurons
- Asynchronous design

## Parallel off-chip interfaces

- Two-phase asynchronous
- Single-ended signaling
- 100-200 MB/s BW

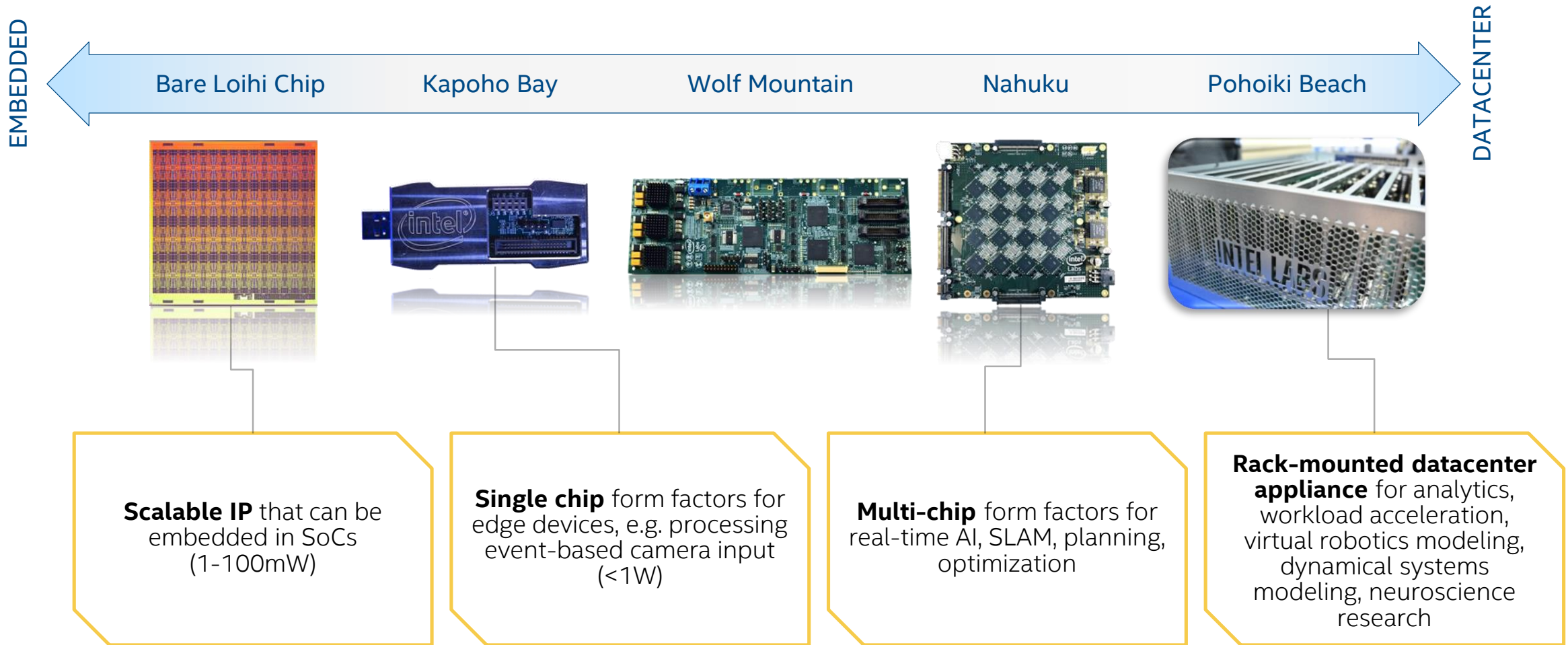
## Low-overhead NoC fabric

- 8x16-core 2D mesh
- Scalable to 1000's cores
- Dimension order routed
- Two physical fabrics
- 8 GB/s per hop

## Embedded x86 processors

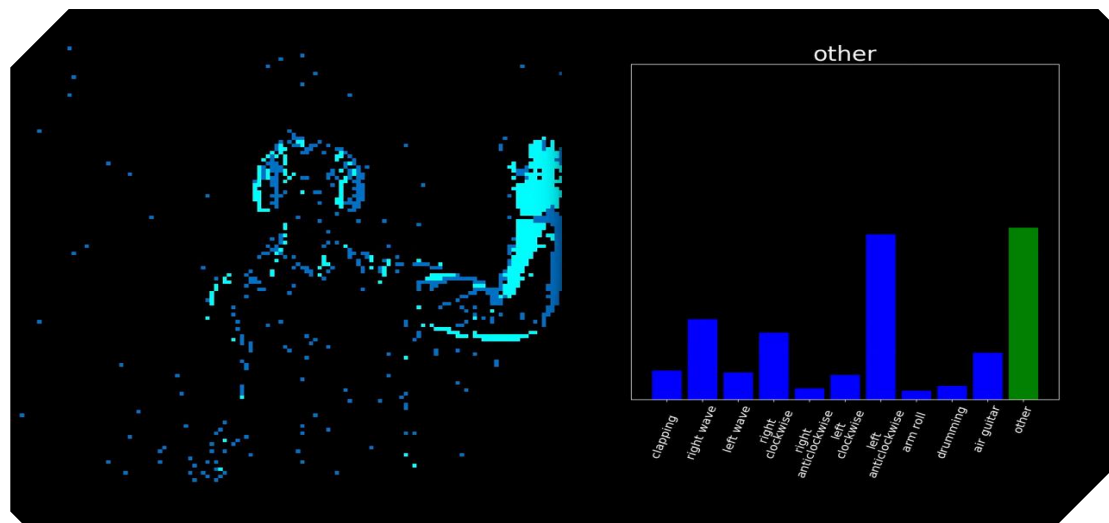
- Efficient spike-based communication with neuromorphic cores
- Data encoding/decoding
- Network configuration
- Synchronous design

# Loihi: form factors



# Loihi and event-based vision

- SLAYER networks



**DAVIS240C\***  
 5mW static  
 5mW dynamic  
 1ms latency



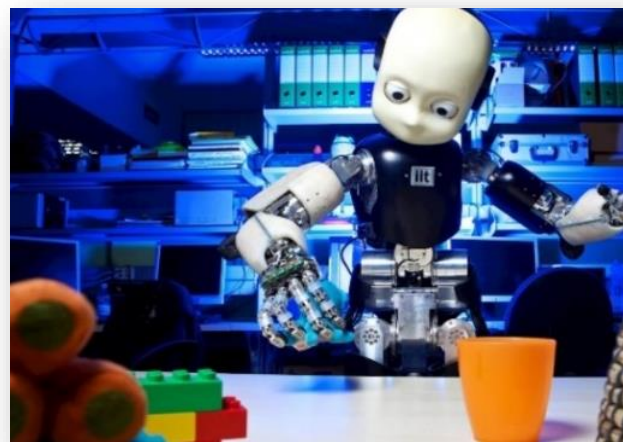
**Loihi†**  
 36mW static  
 7mW dynamic  
 <10ms latency



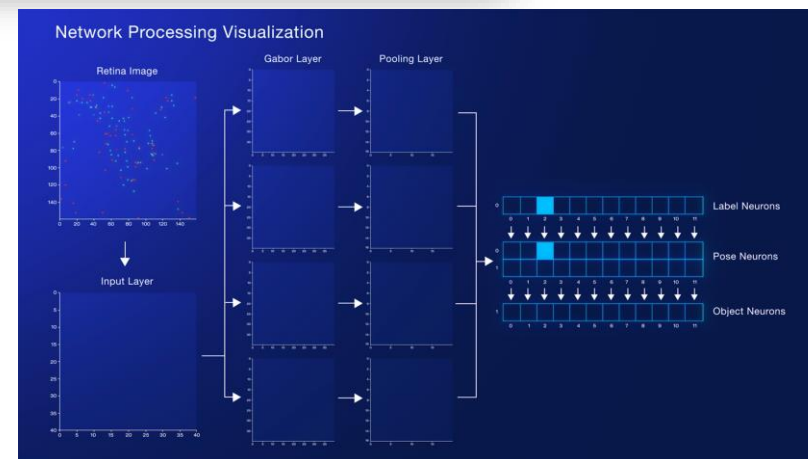
## Deep SNN trained in an event-based manner

See also: K. Stewart, G. Orchard, E. Neftci, [On-chip Few-shot Learning with Surrogate Gradient Descent on a Neuromorphic Processor](#). arXiv:1910.04972, November 2019.

- Fast object learning



- Attention
- State machine
- Spatial memory

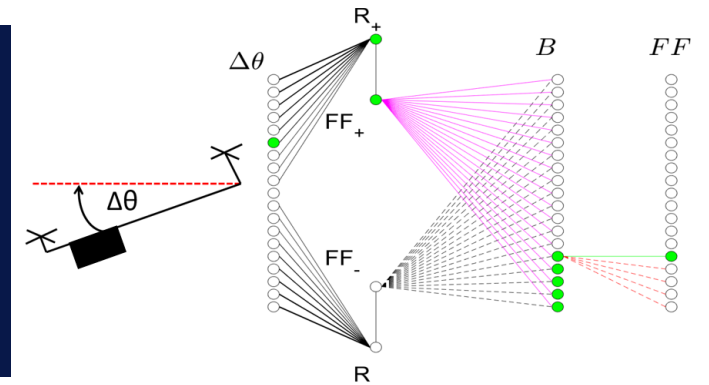
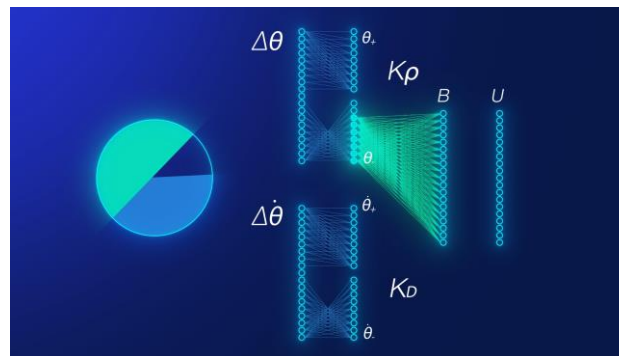
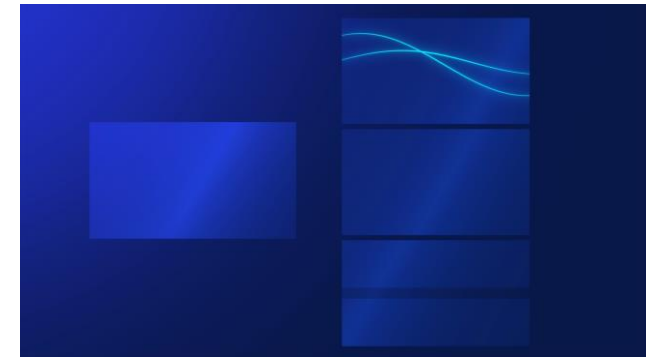
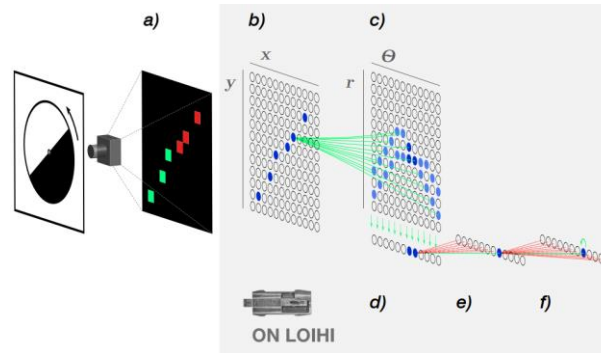


# Adaptive motor control

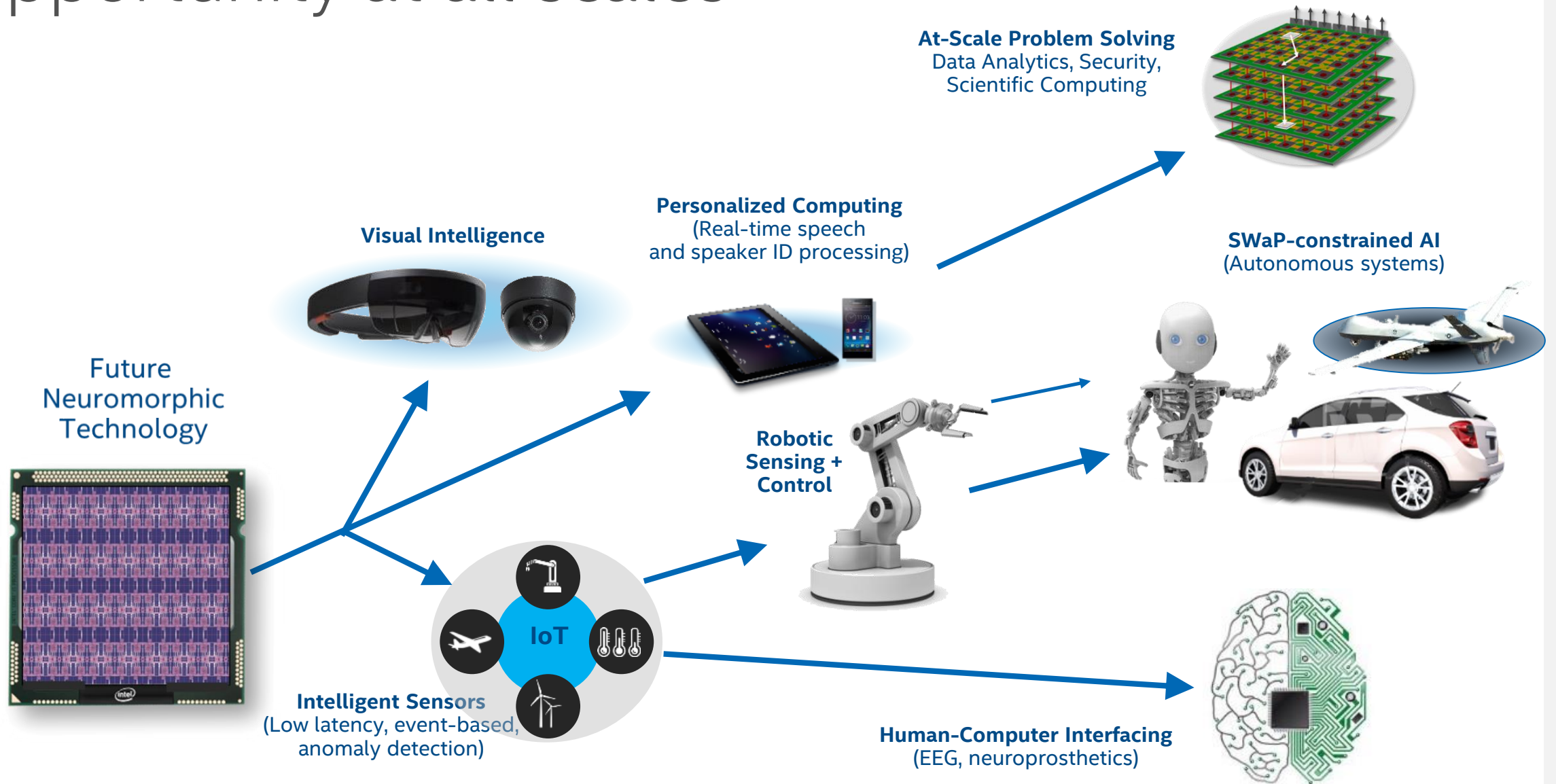
- Enabling the full pipeline from sensing to control on chip
  - Event-based sensing (line tracking) on chip: <200 mks
  - SNN-based control: up to 20kHz vision-driven control rate



Towards Low-Latency High-Bandwidth Control of Quadrotors using Event Cameras. Robotics Perception Group, University of Zurich  
<https://www.youtube.com/watch?v=KfY9j3XAiDU&feature=youtu.be>

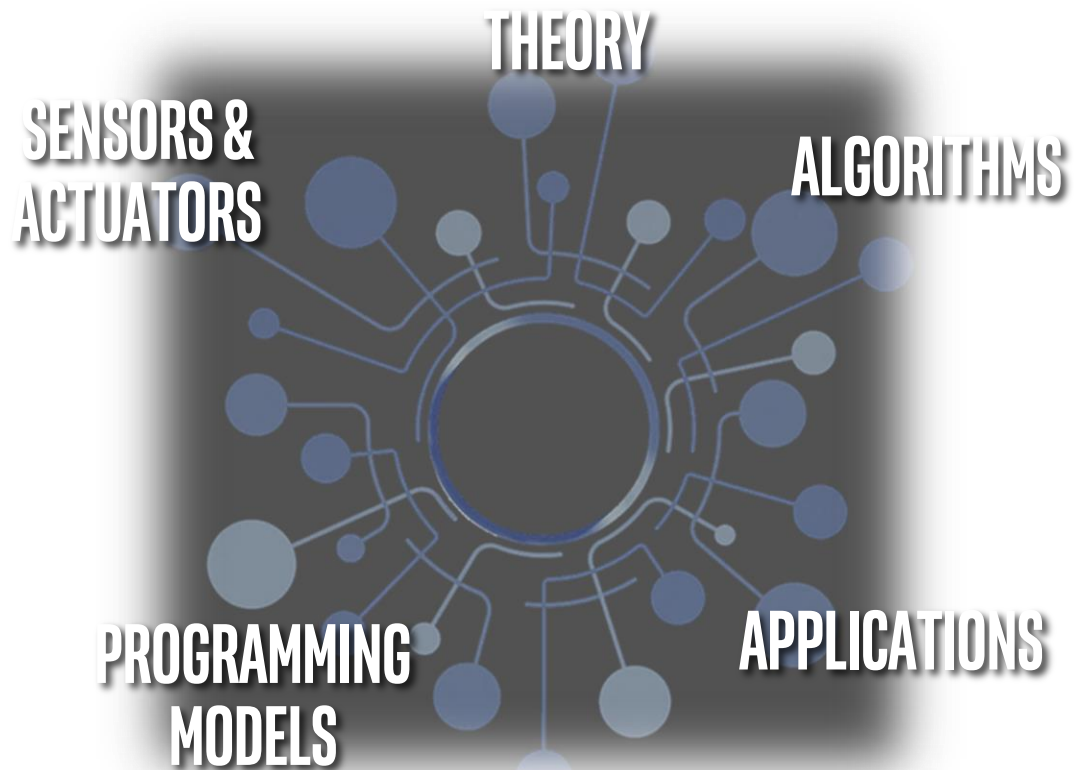


# Opportunity at all scales



# Intel Neuromorphic Research Community

Collaborating to Accelerate the Research



**>100 ENGAGED ACADEMIC, GOVERNMENT, AND INDUSTRY GROUPS**

Email [inrc\\_interest@intel.com](mailto:inrc_interest@intel.com) to get involved!

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